



AO4607

Complementary Enhancement Mode Field Effect Transistor

General Description

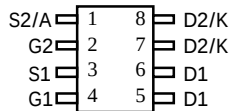
The AO4607 uses advanced trench technology MOSFETs to provide excellent $R_{DS(ON)}$ and low gate charge. The complementary MOSFETs may be used in inverter and other applications. A Schottky diode is co-packaged with the n-channel FET to minimize body diode losses. AO4607 is Pb-free (meets ROHS & Sony 259 specifications). AO4607L is a Green Product ordering option. AO4607 and AO4607L are electrically identical.

Features

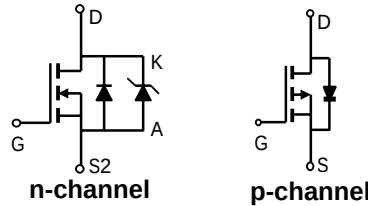
n-channel	p-channel
$V_{DS} (V) = 30V$	-30V
$I_D = 6.9A (V_{GS}=10V)$	-6A ($V_{GS}=1-0V$)
$R_{DS(ON)}$	$R_{DS(ON)}$
$< 28m\Omega (V_{GS}=10V)$	$< 35m\Omega (V_{GS} = -10V)$
$< 42m\Omega (V_{GS}=4.5V)$	$< 58m\Omega (V_{GS} = -4.5V)$

SCHOTTKY

$V_{DS} (V) = 30V$, $I_F = 3A$, $V_F < 0.5V @ 1A$



SOIC-8



n-channel

p-channel

Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Max n-channel	Max p-channel	Units
Drain-Source Voltage	V_{DS}	30	-30	V
Gate-Source Voltage	V_{GS}	± 20	± 20	V
Continuous Drain Current ^A	I_D	6.9	-6	A
$T_A=25^\circ C$		5.8	-5	
Pulsed Drain Current ^B	I_{DM}	30	-30	
Power Dissipation	P_D	2	2	W
$T_A=25^\circ C$		1.28	1.28	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	-55 to 150	$^\circ C$
Parameter	Symbol	Maximum Schottky	Units	
Reverse Voltage	V_{DS}	30	V	
Continuous Forward Current ^A	I_D	3	A	
$T_A=25^\circ C$		2		
Pulsed Diode Forward Current ^B	I_{DM}	20		
Power Dissipation ^A	P_D	2	W	
$T_A=25^\circ C$		1.28		
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$	

Thermal Characteristics: n-channel, Schottky and p-channel						
Parameter		Symbol	Device	Typ	Max	Units
Maximum Junction-to-Ambient ^A	t ≤ 10s	R _{θJA}	n-ch	48	62.5	°C/W
Maximum Junction-to-Ambient ^A	Steady-State		n-ch	74	110	°C/W
Maximum Junction-to-Lead ^C	Steady-State	R _{θJL}	n-ch	35	60	°C/W
Maximum Junction-to-Ambient ^A	t ≤ 10s	R _{θJA}	p-ch	48	62.5	°C/W
Maximum Junction-to-Ambient ^A	Steady-State		p-ch	74	110	°C/W
Maximum Junction-to-Lead ^C	Steady-State	R _{θJL}	p-ch	35	40	°C/W
Maximum Junction-to-Ambient ^A	t ≤ 10s	R _{θJA}	Schottky	47.5	62.5	°C/W
Maximum Junction-to-Ambient ^A	Steady-State		Schottky	71	110	°C/W
Maximum Junction-to-Lead ^C	Steady-State	R _{θJL}	Schottky	32	40	°C/W

N-Channel + Schottky Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =250μA, V _{GS} =0V	30			V
I _{DSS}	Zero Gate Voltage Drain Current. (Set by Schottky leakage)	V _R =30V		0.007	0.05	mA
		V _R =30V, T _J =125°C		3.2	10	
		V _R =30V, T _J =150°C		12	20	
I _{GSS}	Gate-Body leakage current	V _{DS} =0V, V _{GS} =±20V			100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} I _D =250μA	1	1.9	3	V
I _{D(ON)}	On state drain current	V _{GS} =4.5V, V _{DS} =5V	20			A
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =6.9A		22.5	28	mΩ
		T _J =125°C		31.3	38	
		V _{GS} =4.5V, I _D =5.0A		34.5	42	mΩ
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =6.9A	10	15.4		S
V _{SD}	Body-Diode+Schottky Forward Voltage	I _S =1A		0.45	0.5	V
I _S	Maximum Body-Diode+Schottky Continuous Current				5.5	A
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =15V, f=1MHz		680	820	pF
C _{oss}	Output Capacitance (FET+Schottky)			131		pF
C _{rss}	Reverse Transfer Capacitance			77		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		3	3.6	Ω
SWITCHING PARAMETERS						
Q _g (10V)	Total Gate Charge	V _{GS} =10V, V _{DS} =15V, I _D =6.9A		13.84	16.6	nC
Q _g (4.5V)	Total Gate Charge			6.74		nC
Q _{gs}	Gate Source Charge			1.82		nC
Q _{gd}	Gate Drain Charge			3.2		nC
t _{D(on)}	Turn-On DelayTime	V _{GS} =10V, V _{DS} =15V, R _L =2.2Ω, R _{GEN} =3Ω		4.6		ns
t _r	Turn-On Rise Time			4.1		ns
t _{D(off)}	Turn-Off DelayTime			20.6		ns
t _f	Turn-Off Fall Time			5.2		ns
t _{rr}	Body-Diode+Schottky Reverse Recovery Time	I _F =6.9A, dI/dt=100A/μs		13.7	16.5	ns
Q _{rr}	Body-Diode+Schottky Reverse Recovery Charge	I _F =6.9A, dI/dt=100A/μs		4.1		nC

A: The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design. The current rating is based on the t_{ON} ≤ 10s thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C: The R_{θJA} is the sum of the thermal impedance from junction to lead R_{θJL} and lead to ambient.

D: The static characteristics in Figures 1 to 6 are obtained using 80 μs pulses, duty cycle 0.5% max.

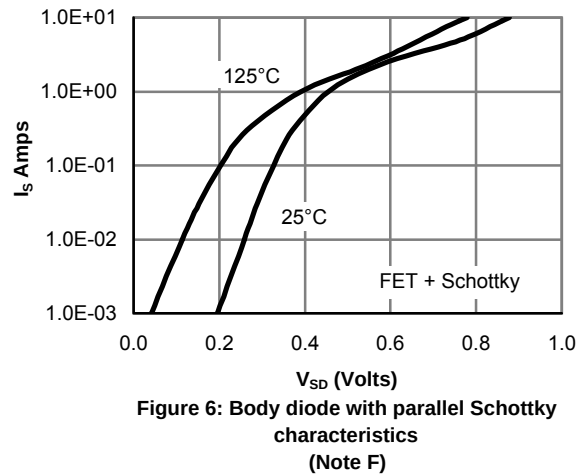
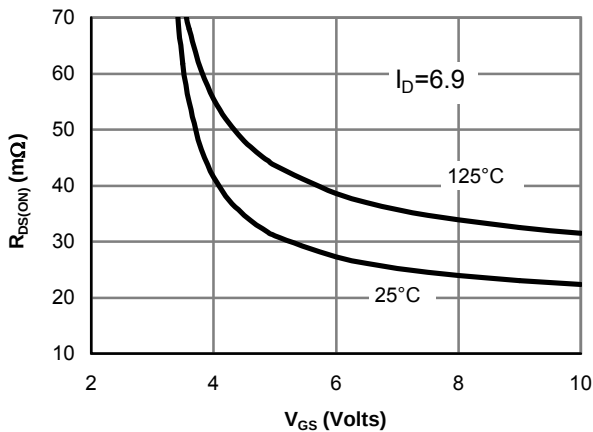
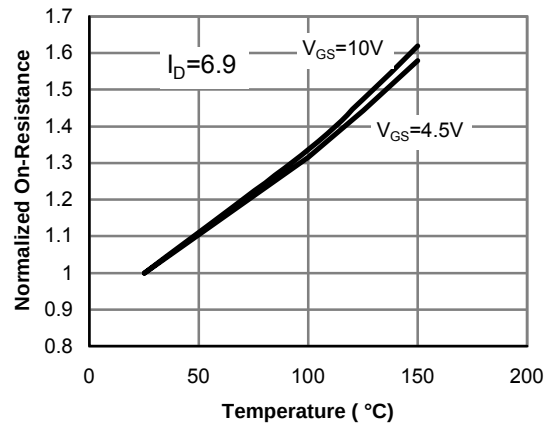
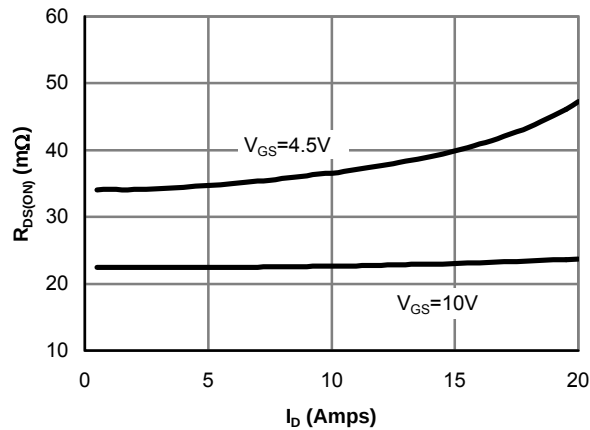
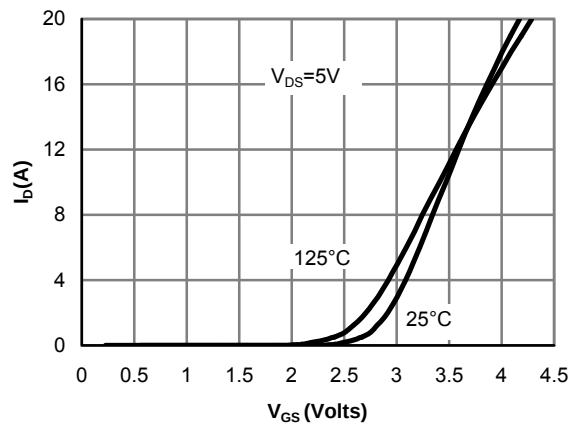
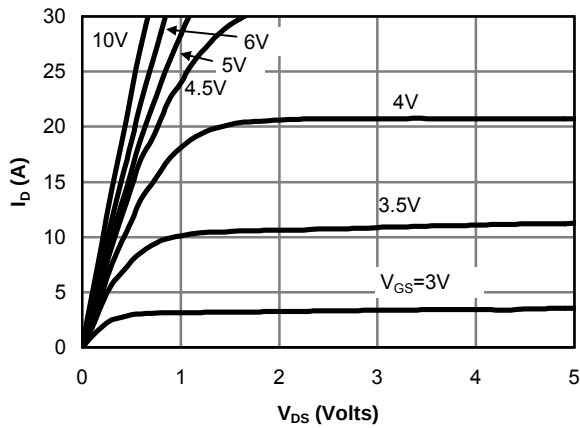
E: These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The SOA curve provides a single pulse rating.

F: The Schottky appears in parallel with the MOSFET body diode, even though it is a separate chip. Therefore, we provide the net forward drop, capacitance and recovery characteristics of the MOSFET and Schottky. However, the thermal resistance is specified for each chip separately

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: N-CHANNEL



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: N-CANNEL

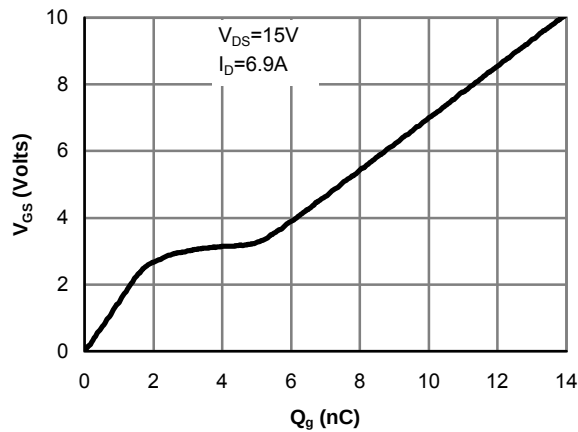


Figure 7: Gate-Charge characteristics

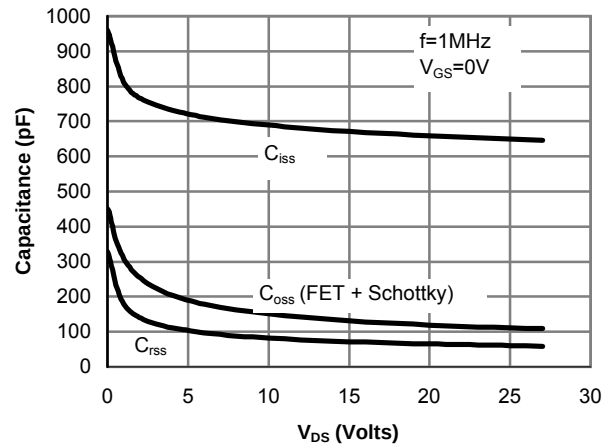


Figure 8: Capacitance Characteristics: MOSFET + Parallel Schottky

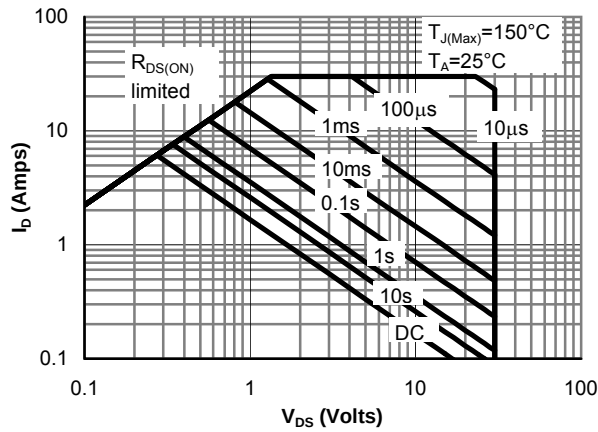


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

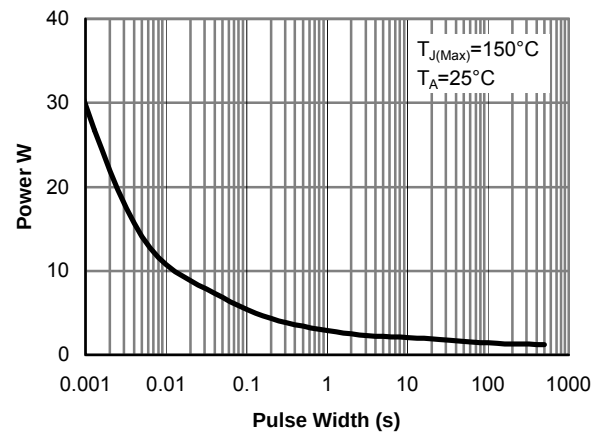


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

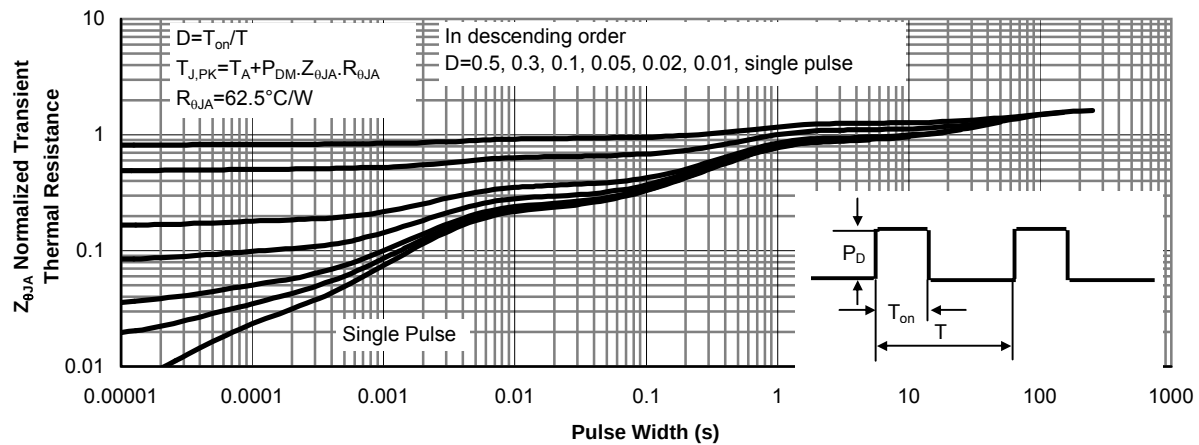


Figure 11: Normalized Maximum Transient Thermal Impedance

P-Channel Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =-250μA, V _{GS} =0V	-30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-24V, V _{GS} =0V T _J =55°C			-1 -5	μA
I _{GSS}	Gate-Body leakage current	V _{DS} =0V, V _{GS} =±20V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1.2	-2	-2.4	V
I _{D(ON)}	On state drain current	V _{GS} =-10V, V _{DS} =-5V	30			A
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-10V, I _D =-6A T _J =125°C		28 37	35 45	mΩ
		V _{GS} =-4.5V, I _D =-5A		44	58	mΩ
g _{FS}	Forward Transconductance	V _{DS} =-5V, I _D =-6A		13		S
V _{SD}	Diode Forward Voltage	I _S =-1A, V _{GS} =0V		-0.76	-1	V
I _S	Maximum Body-Diode Continuous Current				-4.2	A
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-15V, f=1MHz		920	1100	pF
C _{oss}	Output Capacitance			190		pF
C _{rss}	Reverse Transfer Capacitance			122		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		3.6	4.4	Ω
SWITCHING PARAMETERS						
Q _g (10V)	Total Gate Charge (10V)	V _{GS} =-10V, V _{DS} =-15V, I _D =-6A		18.5	22.2	nC
Q _g (4.5V)	Total Gate Charge (4.5V)			9.6		nC
Q _{gs}	Gate Source Charge			2.7		nC
Q _{gd}	Gate Drain Charge			4.5		nC
t _{D(on)}	Turn-On DelayTime	V _{GS} =-10V, V _{DS} =-15V, R _L =2.7Ω, R _{GEN} =3Ω		7.7		ns
t _r	Turn-On Rise Time			5.7		ns
t _{D(off)}	Turn-Off DelayTime			20.2		ns
t _f	Turn-Off Fall Time			9.5		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =-6A, dI/dt=100A/μs		20	24	ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =-6A, dI/dt=100A/μs		8.8		nC

A: The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design. The current rating is based on the t ≤ 10s thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

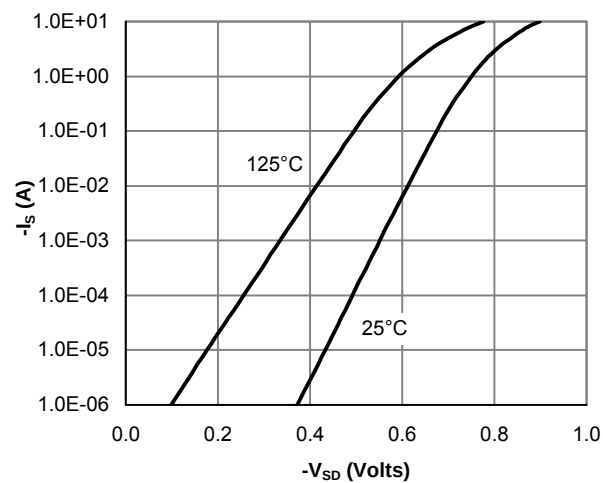
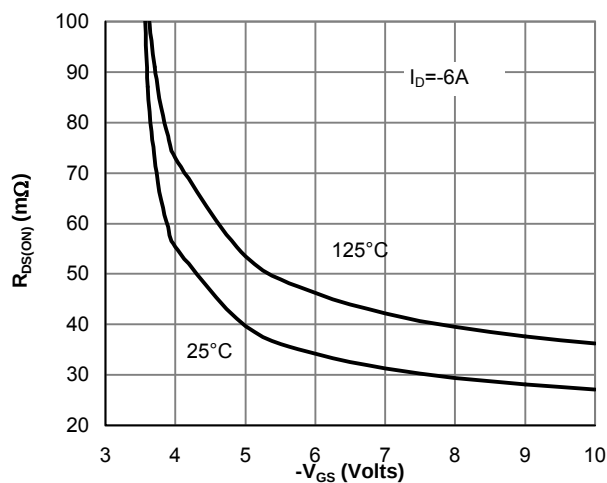
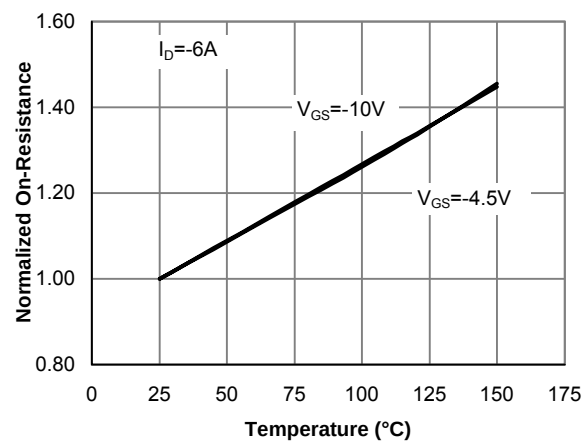
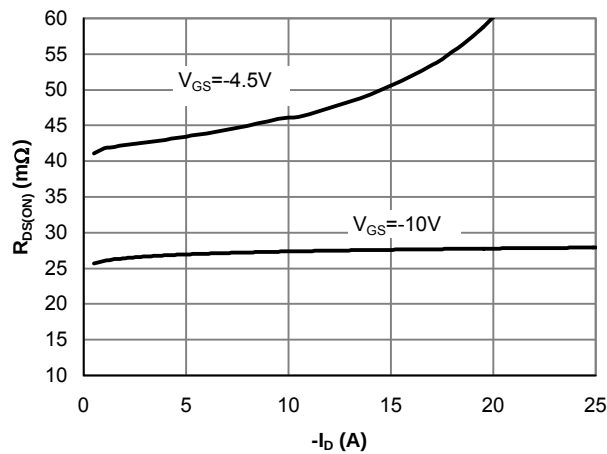
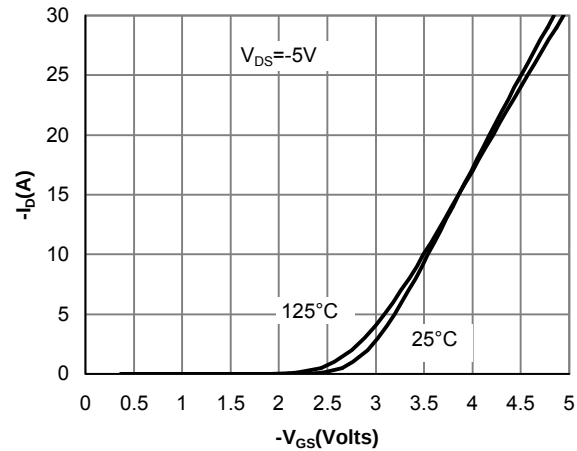
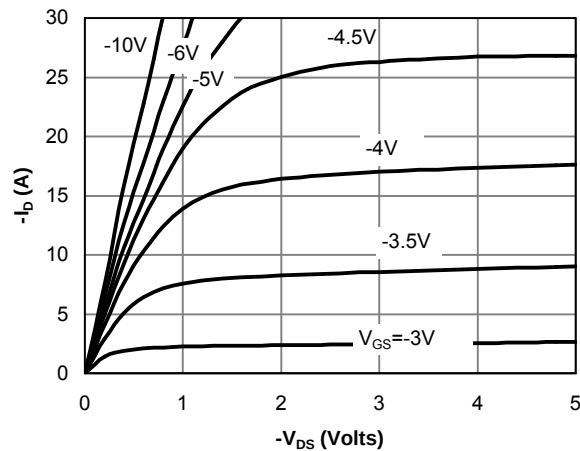
C: The R_{θJA} is the sum of the thermal impedance from junction to lead R_{θJL} and lead to ambient.

D: The static characteristics in Figures 1 to 6,12,14 are obtained using 80 μs pulses, duty cycle 0.5% max.

E: These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The SOA curve provides a single pulse rating.

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