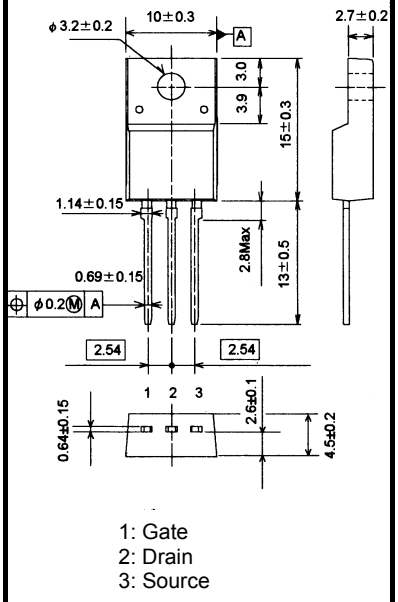
TK7A50D

Switching Regulator Applications

- Low drain-source ON-resistance: $R_{DS\ (ON)}$ = 1.0 Ω (typ.)
- High forward transfer admittance: $|Y_{fs}|$ = 2.5 S (typ.)
- Low leakage current: I_{DSS} = 10 μA (max) (V_{DS} = 500 V)
- Enhancement mode: V_{th} = 2.4 to 4.4 V (V_{DS} = 10 V, I_D = 1 mA)

Unit: mm



1: Gate
2: Drain
3: Source

JEDEC	—
JEITA	SC-67
TOSHIBA	2-10U1B

Weight: 1.7 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

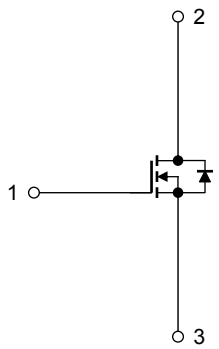
Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	500	V
Gate-source voltage		V_{GSS}	±30	V
Drain current	DC (Note 1)	I_D	7	A
	Pulse (t = 1 ms) (Note 1)	I_{DP}	28	
Drain power dissipation (Tc = 25°C)		P_D	35	W
Single pulse avalanche energy (Note 2)		E_{AS}	129	mJ
Avalanche current		I_{AR}	7	A
Repetitive avalanche energy (Note 3)		E_{AR}	3.5	mJ
Channel temperature		T_{ch}	150	°C
Storage temperature range		T_{stg}	−55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	$R_{th\ (ch-c)}$	3.57	°C/W
Thermal resistance, channel to ambient	$R_{th\ (ch-a)}$	62.5	°C/W

Internal Connection



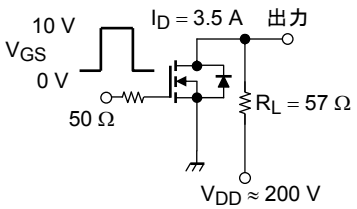
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 4.5 mH, R_G = 25 Ω, I_{AR} = 7 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

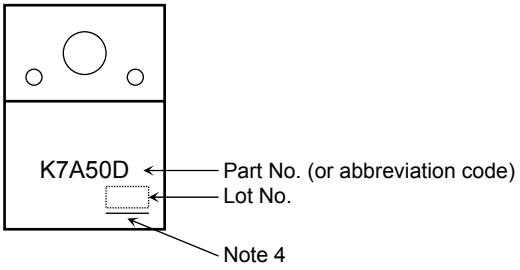
Electrical Characteristics (Ta = 25°C)

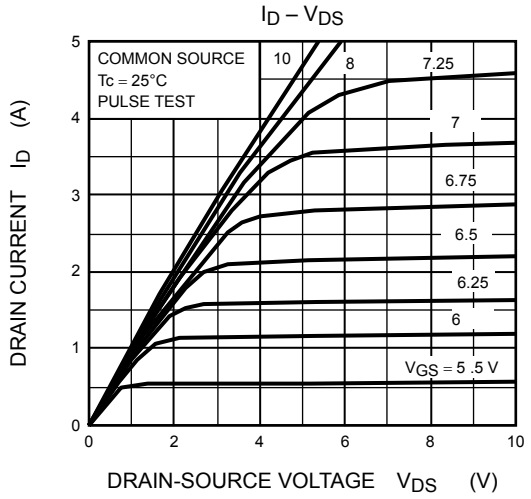
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±30 V, V_{DS} = 0 V	—	—	±1	μA
Drain cut-off current		I_{DSS}	V_{DS} = 500 V, V_{GS} = 0 V	—	—	10	μA
Drain-source breakdown voltage		$V_{(BR)DSS}$	I_D = 10 mA, V_{GS} = 0 V	500	—	—	V
Gate threshold voltage		V_{th}	V_{DS} = 10 V, I_D = 1 mA	2.4	—	4.4	V
Drain-source ON-resistance		$R_{DS(ON)}$	V_{GS} = 10 V, I_D = 3.5 A	—	1.0	1.22	Ω
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} = 10 V, I_D = 3.5 A	0.7	2.5	—	S
Input capacitance		C_{iss}	V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz	—	600	—	pF
Reverse transfer capacitance		C_{rss}		—	4	—	
Output capacitance		C_{oss}		—	70	—	
Switching time	Rise time	t_r	 Duty ≤ 1%, t_w = 10 μs	—	18	—	ns
	Turn-on time	t_{on}		—	40	—	
	Fall time	t_f		—	8	—	
	Turn-off time	t_{off}		—	55	—	
Total gate charge		Q_g	V_{DD} ≈ 400 V, V_{GS} = 10 V, I_D = 7 A	—	12	—	nC
Gate-source charge		Q_{gs}		—	7	—	
Gate-drain charge		Q_{gd}		—	5	—	

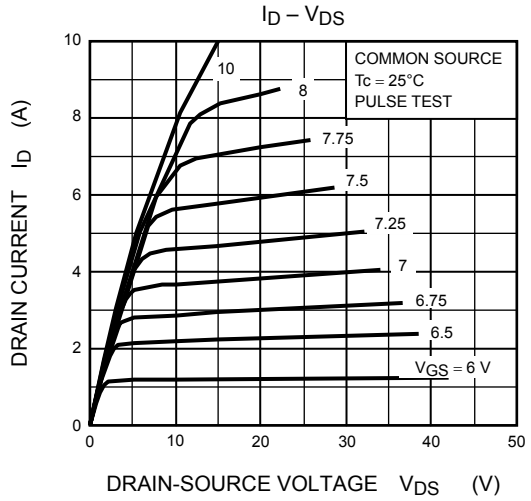
Source-Drain Ratings and Characteristics (Ta = 25°C)

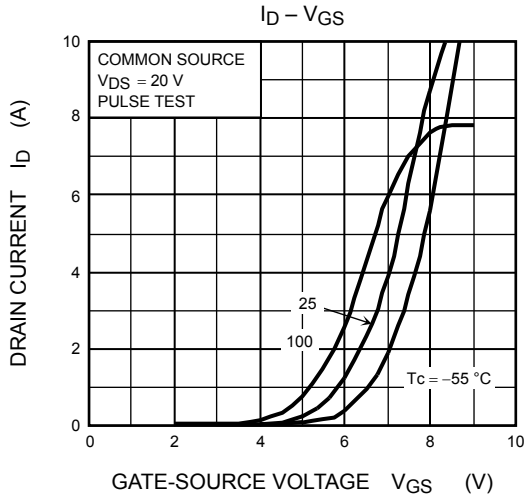
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	—	—	—	7	A
Pulse drain reverse current (Note 1)	I_{DRP}	—	—	—	28	A
Forward voltage (diode)	V_{DSF}	I_{DR} = 7 A, V_{GS} = 0 V	—	—	−1.7	V
Reverse recovery time	t_{rr}	I_{DR} = 7 A, V_{GS} = 0 V, dI_{DR}/dt = 100 A/μs	—	1200	—	ns
Reverse recovery charge	Q_{rr}		—	7	—	μC

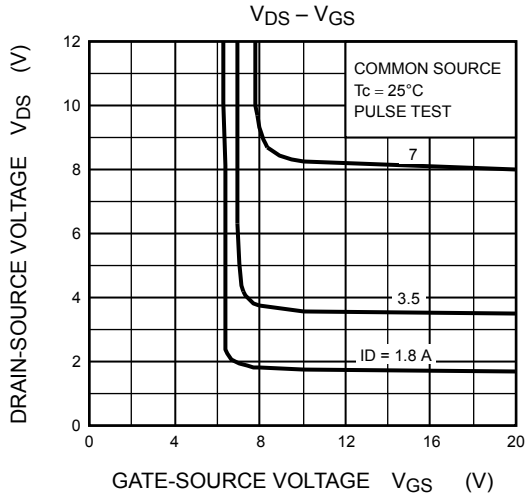
Marking

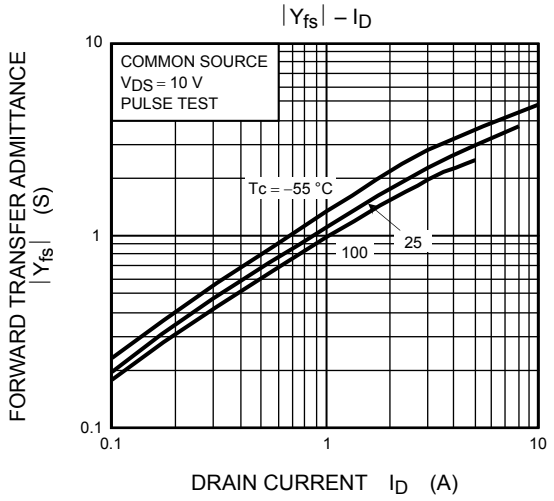


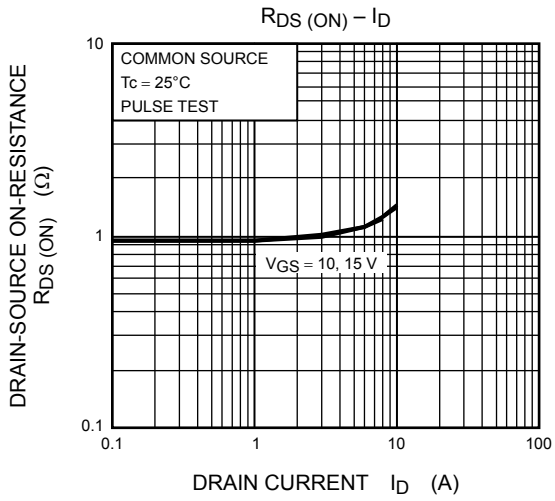

ID – VDS
COMMON SOURCE
Tc = 25°C
PULSE TEST
10
8
7.25
7
6.75
6.5
6.25
6
VGS = 5 .5 V
DRAIN CURRENT ID (A)
DRAIN-SOURCE VOLTAGE VDS (V)


ID – VDS
COMMON SOURCE
Tc = 25°C
PULSE TEST
10
8
7.75
7.5
7.25
7
6.75
6.5
VGS = 6 V
DRAIN CURRENT ID (A)
DRAIN-SOURCE VOLTAGE VDS (V)


ID – VGS
COMMON SOURCE
VDS = 20 V
PULSE TEST
25
100
Tc = –55 °C
DRAIN CURRENT ID (A)
GATE-SOURCE VOLTAGE VGS (V)


VDS – VGS
COMMON SOURCE
Tc = 25°C
PULSE TEST
7
3.5
ID = 1.8 A
DRAIN-SOURCE VOLTAGE VDS (V)
GATE-SOURCE VOLTAGE VGS (V)


|Yfs| – ID
COMMON SOURCE
VDS = 10 V
PULSE TEST
Tc = –55 °C
25
100
FORWARD TRANSFER ADMITTANCE |Yfs| (S)
DRAIN CURRENT ID (A)


RDS (ON) – ID
COMMON SOURCE
Tc = 25°C
PULSE TEST
VGS = 10, 15 V
DRAIN-SOURCE ON-RESISTANCE RDS (ON) (Ω)
DRAIN CURRENT ID (A)