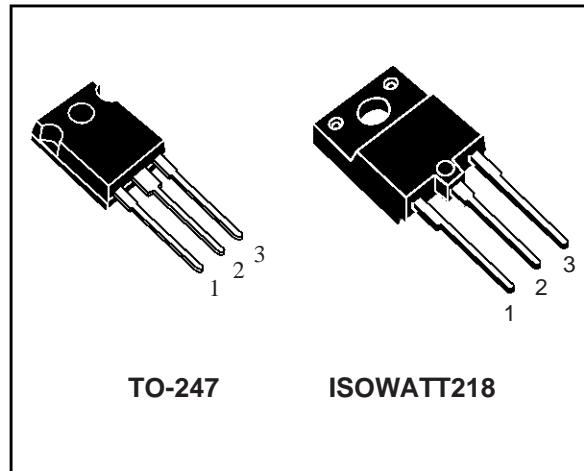


N - CHANNEL ENHANCEMENT MODE FAST POWER MOS TRANSISTOR

TYPE	V _{DSS}	R _{D(on)}	I _D
STW7NA80	800 V	< 1.9 Ω	6.5 A
STH7NA80FI	800 V	< 1.9 Ω	4 A

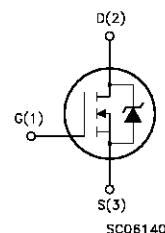
- TYPICAL R_{D(on)} = 1.68 Ω
- ± 30V GATE TO SOURCE VOLTAGE RATING
- 100% AVALANCHE TESTED
- REPETITIVE AVALANCHE DATA AT 100°C
- LOW INTRINSIC CAPACITANCES
- GATE CHARGE MINIMIZED
- REDUCED THRESHOLD VOLTAGE SPREAD



APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SWITCH MODE POWER SUPPLIES (SMPS)
- DC-AC CONVERTERS FOR WELDING EQUIPMENT AND UNINTERRUPTIBLE POWER SUPPLIES AND MOTOR DRIVE

INTERNAL SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
		STW7NA80	STH7NA80FI	
V _{DS}	Drain-source Voltage (V _{GS} = 0)	800	800	V
V _{DGR}	Drain-gate Voltage (R _{GS} = 20 kΩ)	800	800	V
V _{GS}	Gate-source Voltage	± 30	± 30	V
I _D	Drain Current (continuous) at T _c = 25 °C	6.5	4	A
I _D	Drain Current (continuous) at T _c = 100 °C	4	2.5	A
I _{DM(•)}	Drain Current (pulsed)	26	26	A
P _{tot}	Total Dissipation at T _c = 25 °C	150	60	W
	Derating Factor	1.2	0.48	W/°C
V _{ISO}	Insulation Withstand Voltage (DC)	—	4000	V
T _{stg}	Storage Temperature	-65 to 150		°C
T _j	Max. Operating Junction Temperature	150		°C

(•) Pulse width limited by safe operating area

STW7NA80-STH7NA80FI

THERMAL DATA

		TO-247	ISOWATT218	
R _{thj-case}	Thermal Resistance Junction-case	Max	0.83	2.08 °C/W
R _{thj-amb} R _{thc-sink}	Thermal Resistance Junction-ambient Thermal Resistance Case-sink	Max Typ	30 0.1	°C/W °C/W °C
T _I	Maximum Lead Temperature For Soldering Purpose		300	

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I _{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T _j max, δ < 1%)	6.3	A
E _{AS}	Single Pulse Avalanche Energy (starting T _j = 25 °C, I _D = I _{AR} , V _{DD} = 50 V)	320	mJ

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	I _D = 250 μA V _{GS} = 0	800			V
I _{DSS}	Zero Gate Voltage Drain Current (V _{GS} = 0)	V _{DS} = Max Rating V _{DS} = Max Rating T _c = 100 °C			25 50	μA μA
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	V _{GS} = ± 30 V			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} I _D = 250 μA	2.25	3	3.75	V
R _{D(on)}	Static Drain-source On Resistance	V _{GS} = 10V I _D = 3.5 A		1.68	1.9	Ω
I _{D(on)}	On State Drain Current	V _{DS} > I _{D(on)} × R _{D(on)max} V _{GS} = 10 V	7			A

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g _{fs} (*)	Forward Transconductance	V _{DS} > I _{D(on)} × R _{D(on)max} I _D = 3.5 A	4.5	6.3		S
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	V _{DS} = 25 V f = 1 MHz V _{GS} = 0		1330 160 40	1750 210 55	pF pF pF

ELECTRICAL CHARACTERISTICS (continued)

SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$ t_r	Turn-on Time Rise Time	$V_{DD} = 400 \text{ V}$ $I_D = 3.5 \text{ A}$ $R_G = 47 \Omega$ $V_{GS} = 10 \text{ V}$ (see test circuit, figure 3)		3.5 9.5	45 125	ns ns
$(di/dt)_{on}$	Turn-on Current Slope	$V_{DD} = 640 \text{ V}$ $I_D = 7 \text{ A}$ $R_G = 47 \Omega$ $V_{GS} = 10 \text{ V}$ (see test circuit, figure 5)		170		A/ μs
Q_g Q_{gs} Q_{gd}	Total Gate Charge Gate-Source Charge Gate-Drain Charge	$V_{DD} = 640 \text{ V}$ $I_D = 7 \text{ A}$ $V_{GS} = 10 \text{ V}$		58 8 27	78	nC nC nC

SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(loff)}$ t_f t_c	Off-voltage Rise Time Fall Time Cross-over Time	$V_{DD} = 640 \text{ V}$ $I_D = 6 \text{ A}$ $R_G = 47 \Omega$ $V_{GS} = 10 \text{ V}$ (see test circuit, figure 5)		90 25 125	120 35 165	ns ns ns

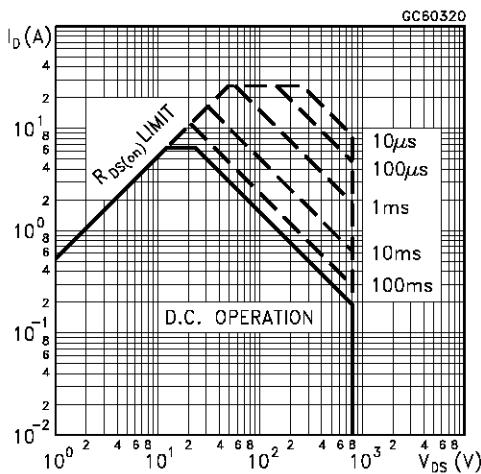
SOURCE DRAIN DIODE

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD} $I_{SDM}(\bullet)$	Source-drain Current Source-drain Current (pulsed)				6.5 26	A A
$V_{SD} (\ast)$	Forward On Voltage	$I_{SD} = 7 \text{ A}$ $V_{GS} = 0$			1.6	V
t_{rr} Q_{rr} I_{RRM}	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current	$I_{SD} = 7 \text{ A}$ $di/dt = 100 \text{ A}/\mu\text{s}$ $V_{DD} = 100 \text{ V}$ $T_j = 150^\circ\text{C}$ (see test circuit, figure 5)		850 15 35		ns μC A

(*) Pulsed: Pulse duration = 300 μs , duty cycle 1.5 %

(*) Pulse width limited by safe operating area

Safe Operating Area for TO-247



Safe Operating Area for ISOWATT218

