

STP4NC60A - STP4NC60AFP STB4NC60A-1

**N-CHANNEL 600V - 1.8Ω - 4.2A TO-220/TO-220FP/I²PAK
PowerMesh™II MOSFET**

TYPE	V _{DSS}	R _{D(on)}	I _D
STP4NC60A	600V	< 2Ω	4.2A
STP4NC60AFP	600V	< 2Ω	4.2A
STB4NC60A-1	600V	< 2Ω	4.2A

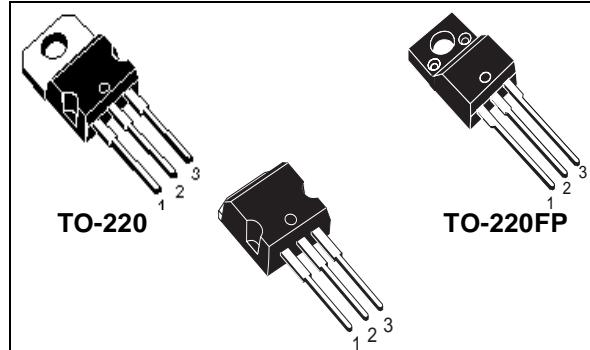
- TYPICAL R_{D(on)} = 1.8Ω
- EXTREMELY HIGH dv/dt CAPABILITY
- 100% AVALANCHE TESTED
- NEW HIGH VOLTAGE BENCHMARK
- GATE CHARGE MINIMIZED

DESCRIPTION

The PowerMESH™II is the evolution of the first generation of MESH OVERLAY™. The layout refinements introduced greatly improve the Ron*area figure of merit while keeping the device at the leading edge for what concerns switching speed, gate charge and ruggedness.

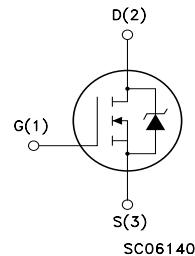
APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SWITH MODE POWER SUPPLIES (SMPS)
- DC-AC CONVERTERS FOR WELDING EQUIPMENT AND UNINTERRUPTIBLE POWER SUPPLIES AND MOTOR DRIVERS



(Tabless TO-220)

INTERNAL SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
		STP(B)4NC60A(-1)	STP4NC60AFP	
V _{DS}	Drain-source Voltage (V _{GS} = 0)	600	600	V
V _{DGR}	Drain-gate Voltage (R _{GS} = 20 kΩ)	600	600	V
V _{GS}	Gate- source Voltage	±30	±30	V
I _D	Drain Current (continuos) at T _C = 25°C	4.2	4.2(*)	A
I _D	Drain Current (continuos) at T _C = 100°C	2.6	2.6(*)	A
I _{DM} (•)	Drain Current (pulsed)	16.8	16.8(*)	A
P _{TOT}	Total Dissipation at T _C = 25°C	100	35	W
	Derating Factor	0.8	0.28	W/°C
dv/dt(1)	Peak Diode Recovery voltage slope	3.5	3.5	V/ns
V _{ISO}	Insulation Withstand Voltage (DC)	-	2500	V
T _{stg}	Storage Temperature	-60 to 150		°C
T _j	Max. Operating Junction Temperature	-60 to 150		°C

(•)Pulse width limited by safe operating area

(*)Limited only by maximum Temperature allowed

(1)I_{SD} ≤ 4.2A, d_i/d_t ≤ 300A/μs, V_{DD} ≤ V_{(BR)DSS}, T_j ≤ T_{JMAX}.

STP4NC60A/FP/STB4NC60A-1

THERMAL DATA

		TO-220/I ² PAK	TO-220FP	
R _{thj-case}	Thermal Resistance Junction-case Max	1.25	3.57	°C/W
R _{thj-amb} T _I	Thermal Resistance Junction-ambient Max Maximum Lead Temperature For Soldering Purpose	62.5 300		°C/W °C

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I _{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T _j max)	4.2	A
E _{AS}	Single Pulse Avalanche Energy (starting T _j = 25 °C, I _D = I _{AR} , V _{DD} = 50 V)	250	mJ

ELECTRICAL CHARACTERISTICS (TCASE = 25 °C UNLESS OTHERWISE SPECIFIED) OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	I _D = 250 μA, V _{GS} = 0	600			V
I _{DSS}	Zero Gate Voltage Drain Current (V _{GS} = 0)	V _{DS} = Max Rating V _{DS} = Max Rating, T _C = 125 °C			1 50	μA μA
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	V _{GS} = ±30V			±100	nA

ON (1)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2	3	4	V
R _{DSS(on)}	Static Drain-source On Resistance	V _{GS} = 10V, I _D = 1.5 A		1.8	2	Ω

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g _{fs} (1)	Forward Transconductance	V _{DS} > I _{D(on)} × R _{DSS(on)max} , I _D = 2A		3.7		S
C _{iss}	Input Capacitance	V _{DS} = 25V, f = 1 MHz, V _{GS} = 0		475		pF
C _{oss}	Output Capacitance			72		pF
C _{rss}	Reverse Transfer Capacitance			10		pF

ELECTRICAL CHARACTERISTICS (CONTINUED)**SWITCHING ON**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = 300V, I_D = 2A$ $R_G = 4.7\Omega, V_{GS} = 10V$ (see test circuit, Figure 3)		14		ns
t_r	Rise Time			14		ns
Q_g	Total Gate Charge	$V_{DD} = 480V, I_D = 4A,$ $V_{GS} = 10V$		16.5	21.1	nC
Q_{gs}	Gate-Source Charge			2.5		nC
Q_{gd}	Gate-Drain Charge			9		nC

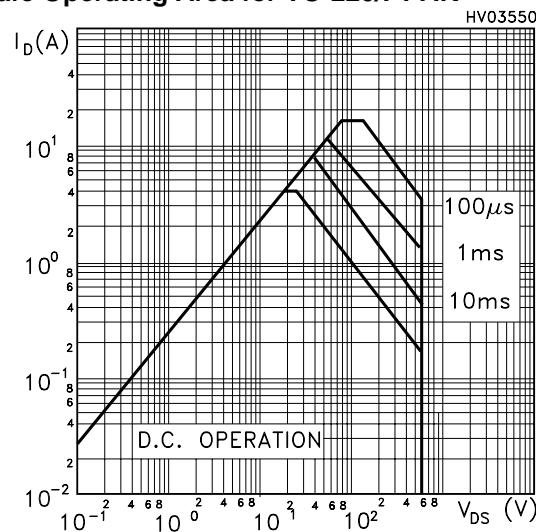
SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(V_{off})}$	Off-voltage Rise Time	$V_{DD} = 480V, I_D = 4A,$ $R_G = 4.7\Omega, V_{GS} = 10V$ (see test circuit, Figure 5)		15		ns
t_f	Fall Time			19		ns
t_c	Cross-over Time			24		ns

SOURCE DRAIN DIODE

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD}	Source-drain Current				4.2	A
$I_{SDM(2)}$	Source-drain Current (pulsed)				16.8	A
$V_{SD}(1)$	Forward On Voltage	$I_{SD} = 4.2A, V_{GS} = 0$			1.6	V
t_{rr}	Reverse Recovery Time	$I_{SD} = 4A, di/dt = 100A/\mu s,$ $V_{DD} = 100V, T_j = 150^\circ C$ (see test circuit, Figure 5)		600		ns
Q_{rr}	Reverse Recovery Charge			2.7		μC
I_{RRM}	Reverse Recovery Current			9		A

Note: 1. Pulsed: Pulse duration = 300 μs , duty cycle 1.5 %.
2. Pulse width limited by safe operating area.

Safe Operating Area for TO-220/I²PAK**Safe Operating Area for TO-220FP**