

FDP8874

N-Channel PowerTrench® MOSFET 30V, 114A, 5.3mΩ

General Description

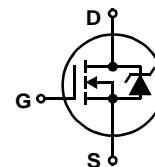
This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low $r_{DS(ON)}$ and fast switching speed.

Applications

- DC/DC converters

Features

- $r_{DS(ON)} = 5.3\text{m}\Omega$, $V_{GS} = 10\text{V}$, $I_D = 40\text{A}$
- $r_{DS(ON)} = 6.6\text{m}\Omega$, $V_{GS} = 4.5\text{V}$, $I_D = 40\text{A}$
- High performance trench technology for extremely low $r_{DS(ON)}$
- Low gate charge
- High power and current handling capability
- RoHS Compliant



MOSFET Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DSS}	Drain to Source Voltage	30	V
V_{GS}	Gate to Source Voltage	± 20	V
I_D	Drain Current Continuous ($T_C = 25^\circ\text{C}$, $V_{GS} = 10\text{V}$) (Note 1)	114	A
	Continuous ($T_C = 25^\circ\text{C}$, $V_{GS} = 4.5\text{V}$) (Note 1)	102	A
	Continuous ($T_{amb} = 25^\circ\text{C}$, $V_{GS} = 10\text{V}$, with $R_{\theta JA} = 62^\circ\text{C}/\text{W}$)	16	A
	Pulsed	Figure 4	A
E_{AS}	Single Pulse Avalanche Energy (Note 2)	105	mJ
P_D	Power dissipation	110	W
	Derate above 25°C	0.73	$\text{W}/^\circ\text{C}$
T_J , T_{STG}	Operating and Storage Temperature	-55 to 175	$^\circ\text{C}$

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance Junction to Case TO-220	1.36	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance Junction to Ambient TO-220 (Note 3)	62	$^\circ\text{C}/\text{W}$

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP8874	FDP8874	TO-220AB	Tube	N/A	50 units

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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Off Characteristics

B_{VDSS}	Drain to Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24\text{V}$	-	-	1	μA
		$V_{GS} = 0\text{V}$	$T_C = 150^\circ\text{C}$	-	250	
I_{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 20\text{V}$	-	-	± 100	nA

On Characteristics

$V_{GS(TH)}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	1.2	-	2.5	V
$r_{DS(ON)}$	Drain to Source On Resistance	$I_D = 40\text{A}, V_{GS} = 10\text{V}$	-	0.0036	0.0053	Ω
		$I_D = 40\text{A}, V_{GS} = 4.5\text{V}$	-	0.0045	0.0066	
		$I_D = 40\text{A}, V_{GS} = 10\text{V}, T_J = 175^\circ\text{C}$	-	0.0062	0.0090	

Dynamic Characteristics

C_{ISS}	Input Capacitance	$V_{DS} = 15\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$	-	3130	-	pF
C_{OSS}	Output Capacitance		-	590	-	pF
C_{RSS}	Reverse Transfer Capacitance		-	345	-	pF
R_G	Gate Resistance	$V_{GS} = 0.5\text{V}, f = 1\text{MHz}$	-	1.9	-	Ω
$Q_{g(TOT)}$	Total Gate Charge at 10V	$V_{GS} = 0\text{V to } 10\text{V}$	-	56	72	nc
$Q_{g(5)}$	Total Gate Charge at 5V	$V_{GS} = 0\text{V to } 5\text{V}$	-	30	38	nc
$Q_{g(TH)}$	Threshold Gate Charge	$V_{GS} = 0\text{V to } 1\text{V}$	$V_{DD} = 15\text{V}$ $I_D = 40\text{A}$ $I_g = 1.0\text{mA}$	3.0	4.0	nc
Q_{gs}	Gate to Source Gate Charge	9.0		-	nc	
Q_{gs2}	Gate Charge Threshold to Plateau	6.0		-	nc	
Q_{gd}	Gate to Drain "Miller" Charge	11		-	nc	

Switching Characteristics ($V_{GS} = 10\text{V}$)

t_{ON}	Turn-On Time	$V_{DD} = 15\text{V}, I_D = 40\text{A}$ $V_{GS} = 4.5\text{V}, R_{GS} = 4.7\Omega$	-	-	207	ns
$t_{d(ON)}$	Turn-On Delay Time		-	10	-	ns
t_r	Rise Time		-	128	-	ns
$t_{d(OFF)}$	Turn-Off Delay Time		-	44	-	ns
t_f	Fall Time		-	31	-	ns
t_{OFF}	Turn-Off Time		-	-	112	ns

Drain-Source Diode Characteristics

V_{SD}	Source to Drain Diode Voltage	$I_{SD} = 40\text{A}$	-	-	1.25	V
		$I_{SD} = 20\text{A}$	-	-	1.0	V
t_{rr}	Reverse Recovery Time	$I_{SD} = 40\text{A}, dI_{SD}/dt = 100\text{A}/\mu\text{s}$	-	-	32	ns
Q_{RR}	Reverse Recovered Charge	$I_{SD} = 40\text{A}, dI_{SD}/dt = 100\text{A}/\mu\text{s}$	-	-	18	nc

Notes:

- 1: Package current limitation is 80A.
- 2: Starting $T_J = 25^\circ\text{C}$, $L = 51\mu\text{H}$, $I_{AS} = 64\text{A}$, $V_{DD} = 27\text{V}$, $V_{GS} = 10\text{V}$.
- 3: Pulse width = 100s.

Typical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

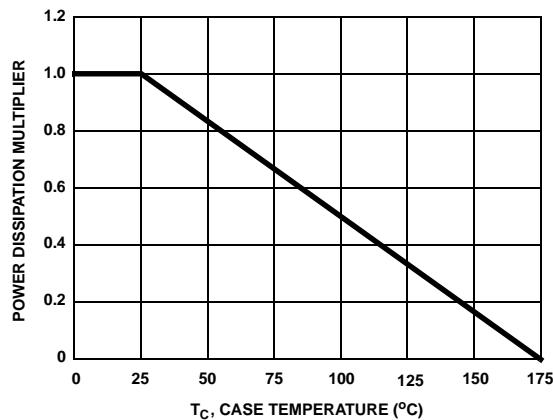


Figure 1. Normalized Power Dissipation vs Case Temperature

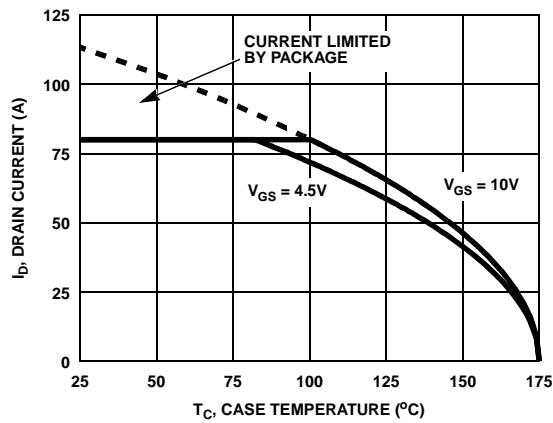


Figure 2. Maximum Continuous Drain Current vs Case Temperature

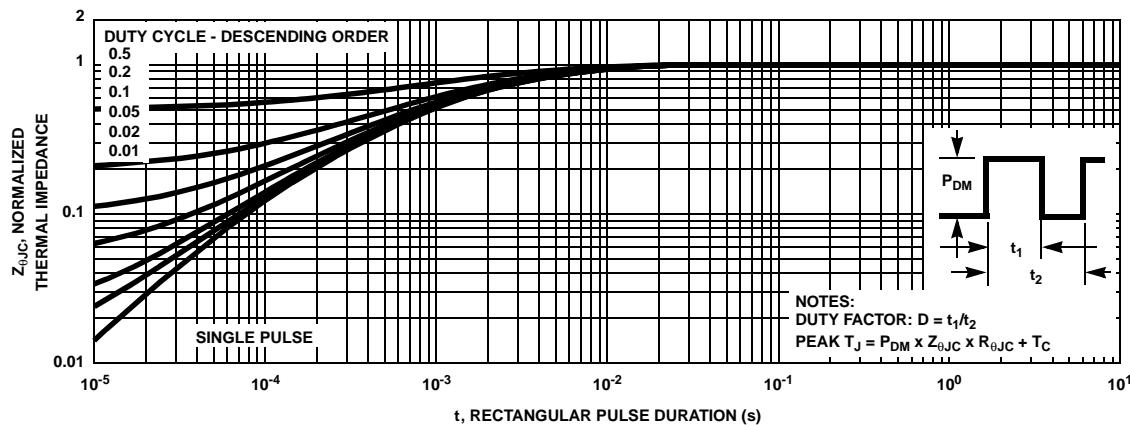


Figure 3. Normalized Maximum Transient Thermal Impedance

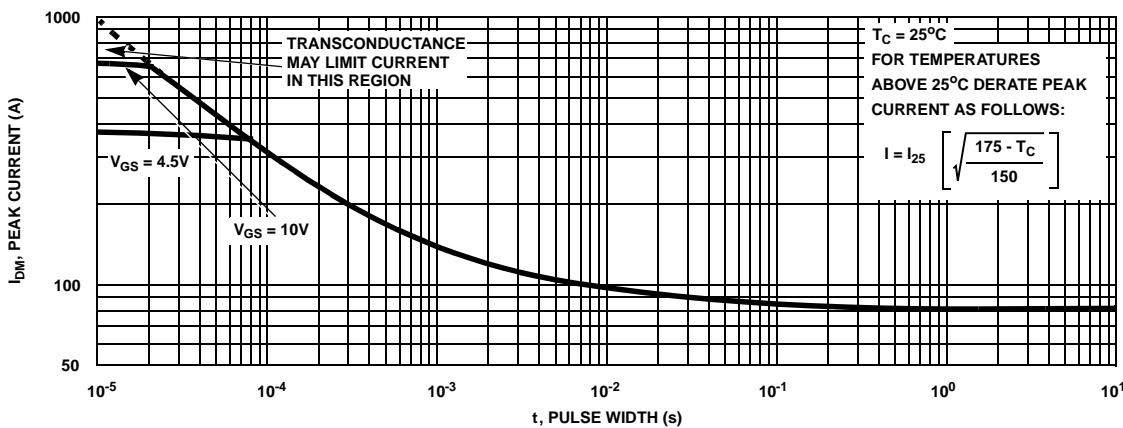


Figure 4. Peak Current Capability