



FDP75N08A

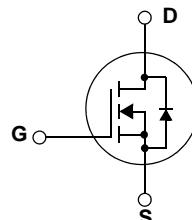
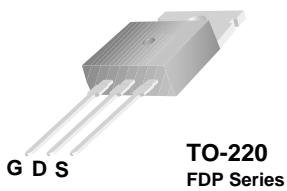
75V N-Channel MOSFET

Features

- 75A, 75V, $R_{DS(on)} = 0.011\Omega$ @ $V_{GS} = 10$ V
- Low gate charge (typical 145nC)
- Low Crss (typical 86pF)
- Fast switching
- Improved dv/dt capability

Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficient switched mode power supplies, active power factor correction, electronic lamp ballast based on half bridge topology.



Absolute Maximum Ratings

Symbol	Parameter	FDP75N08A	Units
V_{DSS}	Drain-Source Voltage	75	V
I_D	Drain Current - Continuous ($T_C = 25^\circ C$)	75	A
	- Continuous ($T_C = 100^\circ C$)	47	A
I_{DM}	Drain Current - Pulsed	(Note 1)	A
V_{GSS}	Gate-Source Voltage	± 20	V
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	mJ
I_{AR}	Avalanche Current	(Note 1)	A
E_{AR}	Repetitive Avalanche Energy	(Note 1)	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	V/ns
P_D	Power Dissipation ($T_C = 25^\circ C$)	137	W
	- Derate above $25^\circ C$	1.09	W/ $^\circ C$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ C$
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	$^\circ C$

Thermal Characteristics

Symbol	Parameter	FDP75N08A	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.91	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	$^\circ C/W$

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP75N08A	FDP75N08A	TO-220	--	--	50

Electrical Characteristics

$T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}$, $I_D = 250 \mu\text{A}$	75	--	--	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$, Referenced to 25°C	--	0.6	--	$\text{V}/^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 75 \text{ V}$, $V_{\text{GS}} = 0 \text{ V}$	--	--	1	μA
		$V_{\text{DS}} = 60 \text{ V}$, $T_C = 125^\circ\text{C}$	--	--	10	μA
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{\text{GS}} = 20 \text{ V}$, $V_{\text{DS}} = 0 \text{ V}$	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{\text{GS}} = -20 \text{ V}$, $V_{\text{DS}} = 0 \text{ V}$	--	--	-100	nA
On Characteristics						
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}$, $I_D = 250 \mu\text{A}$	2.0	--	4.0	V
$R_{\text{DS(on)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}} = 10 \text{ V}$, $I_D = 37.5 \text{ A}$	--	9.4	11	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{\text{DS}} = 40 \text{ V}$, $I_D = 37.5 \text{ A}$	(Note 4)	--	15	--
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}} = 25 \text{ V}$, $V_{\text{GS}} = 0 \text{ V}$, $f = 1.0 \text{ MHz}$	--	3437	4468	pF
C_{oss}	Output Capacitance		--	738	959	pF
C_{rss}	Reverse Transfer Capacitance		--	86	129	pF
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}} = 37.5 \text{ V}$, $I_D = 75 \text{ A}$, $R_G = 25 \Omega$	--	43	95	ns
t_r	Turn-On Rise Time		--	212	434	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	273	556	ns
t_f	Turn-Off Fall Time		--	147	303	ns
Q_g	Total Gate Charge	$V_{\text{DS}} = 60 \text{ V}$, $I_D = 75 \text{ A}$, $V_{\text{GS}} = 10 \text{ V}$	--	80	104	nC
Q_{gs}	Gate-Source Charge		--	20	--	nC
Q_{gd}	Gate-Drain Charge		--	24	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current	--	--	75	--	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current	--	--	300	--	A
V_{SD}	Drain-Source Diode Forward Voltage	$V_{\text{GS}} = 0 \text{ V}$, $I_S = 75 \text{ A}$	--	--	1.4	V
t_{rr}	Reverse Recovery Time	$V_{\text{GS}} = 0 \text{ V}$, $I_S = 75 \text{ A}$, $dI_F/dt = 100 \text{ A}/\mu\text{s}$	(Note 4)	--	62	--
Q_{rr}	Reverse Recovery Charge	--	--	145	--	nC

NOTES:

1. Repetitive Rating : Pulse width limited by maximum junction temperature

2. $L = 206 \mu\text{H}$, $I_{AS} = 75 \text{ A}$, $V_{DD} = 50 \text{ V}$, $R_G = 25 \Omega$, Starting $T_J = 25^\circ\text{C}$

3. $I_{SD} \leq 75 \text{ A}$, $dI/dt \leq 200 \text{ A}/\mu\text{s}$, $V_{DD} \leq \text{BV}_{\text{DSS}}$, Starting $T_J = 25^\circ\text{C}$

4. Pulse Test : Pulse width $\leq 300 \mu\text{s}$, Duty cycle $\leq 2\%$

5. Essentially independent of operating temperature