



FDB3652 / FDP3652 / FDI3652

N-Channel PowerTrench® MOSFET
100V, 61A, 16mΩ

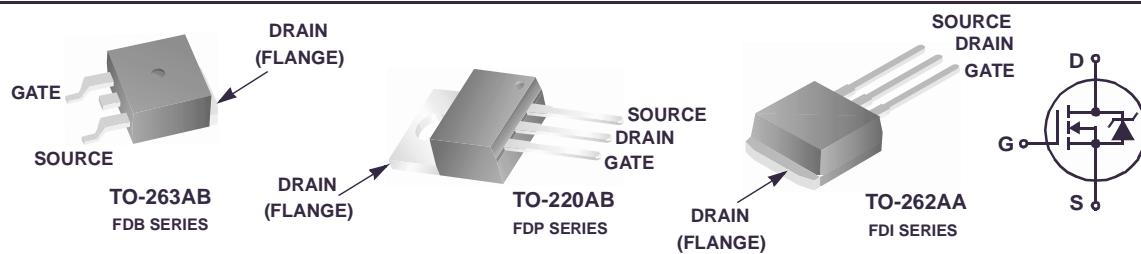
Features

- $r_{DS(ON)} = 14\text{m}\Omega$ (Typ.), $V_{GS} = 10\text{V}$, $I_D = 61\text{A}$
- $Q_g(\text{tot}) = 41\text{nC}$ (Typ.), $V_{GS} = 10\text{V}$
- Low Miller Charge
- Low Q_{RR} Body Diode
- UIS Capability (Single Pulse and Repetitive Pulse)
- Qualified to AEC Q101

Formerly developmental type 82769

Applications

- DC/DC Converters and Off-line UPS
- Distributed Power Architectures and VRMs
- Primary Switch for 24V and 48V Systems
- High Voltage Synchronous Rectifier
- Direct Injection / Diesel Injection Systems
- 42V Automotive Load Control
- Electronic Valve Train Systems



MOSFET Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DSS}	Drain to Source Voltage	100	V
V_{GS}	Gate to Source Voltage	± 20	V
I_D	Drain Current Continuous ($T_C = 25^\circ\text{C}$, $V_{GS} = 10\text{V}$)	61	A
	Continuous ($T_C = 100^\circ\text{C}$, $V_{GS} = 10\text{V}$)	43	A
	Continuous ($T_{amb} = 25^\circ\text{C}$, $V_{GS} = 10\text{V}$) with $R_{\theta JA} = 43^\circ\text{C/W}$	9	A
	Pulsed	Figure 4	A
E_{AS}	Single Pulse Avalanche Energy (Note 1)	182	mJ
P_D	Power dissipation	150	W
	Derate above 25°C	1.0	W/ $^\circ\text{C}$
T_J , T_{STG}	Operating and Storage Temperature	-55 to 175	$^\circ\text{C}$

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance Junction to Case TO-220, TO-263, TO-262	1.0	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction to Ambient TO-220, TO-263, TO-262 (Note 2)	62	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction to Ambient TO-263, 1in ² copper pad area	43	$^\circ\text{C/W}$

This product has been designed to meet the extreme test conditions and environment demanded by the automotive industry. For a copy of the requirements, see AEC Q101 at: <http://www.aecouncil.com/>

Reliability data can be found at: <http://www.fairchildsemi.com/products/discrete/reliability/index.html>.

All Fairchild Semiconductor products are manufactured, assembled and tested under ISO9000 and QS9000 quality systems certification.

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDB3652	FDB3652	TO-263AB	330mm	24mm	800 units
FDP3652	FDP3652	TO-220AB	Tube	N/A	50 units
FDI3652	FDI3652	TO-262AA	Tube	N/A	50 units

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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Off Characteristics

B_{VDSS}	Drain to Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	105	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 80\text{V}$	-	-	1	μA
		$V_{GS} = 0\text{V}$	$T_C = 150^\circ\text{C}$	-	-	
I_{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 20\text{V}$	-	-	± 100	nA

On Characteristics

$V_{GS(TH)}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	2	-	4	V
$r_{DS(ON)}$	Drain to Source On Resistance	$I_D = 61\text{A}, V_{GS} = 10\text{V}$	-	0.014	0.016	Ω
		$I_D = 30\text{A}, V_{GS} = 6\text{V}$	-	0.018	0.026	
		$I_D = 61\text{A}, V_{GS} = 10\text{V}, T_J = 175^\circ\text{C}$	-	0.035	0.043	

Dynamic Characteristics

C_{ISS}	Input Capacitance	$V_{DS} = 25\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$	-	2880	-	pF
C_{OSS}	Output Capacitance		-	390	-	pF
C_{RSS}	Reverse Transfer Capacitance		-	100	-	pF
$Q_g(\text{TOT})$	Total Gate Charge at 10V	$V_{GS} = 0\text{V to } 10\text{V}$	-	41	53	nC
$Q_g(\text{TH})$	Threshold Gate Charge		-	5	6.5	nC
Q_{gs}	Gate to Source Gate Charge		-	15	-	nC
Q_{gs2}	Gate Charge Threshold to Plateau		-	10	-	nC
Q_{gd}	Gate to Drain "Miller" Charge		-	10	-	nC

Switching Characteristics ($V_{GS} = 10\text{V}$)

t_{ON}	Turn-On Time	$V_{DD} = 50\text{V}, I_D = 61\text{A}$	-	-	146	ns
$t_{d(ON)}$	Turn-On Delay Time		-	12	-	ns
t_r	Rise Time		-	85	-	ns
$t_{d(OFF)}$	Turn-Off Delay Time		-	26	-	ns
t_f	Fall Time		-	45	-	ns
t_{OFF}	Turn-Off Time		-	-	107	ns

Drain-Source Diode Characteristics

V_{SD}	Source to Drain Diode Voltage	$I_{SD} = 61\text{A}$	-	-	1.25	V
		$I_{SD} = 30\text{A}$	-	-	1.0	V
t_{rr}	Reverse Recovery Time	$I_{SD} = 61\text{A}, dI_{SD}/dt = 100\text{A}/\mu\text{s}$	-	-	62	ns
Q_{RR}	Reverse Recovered Charge	$I_{SD} = 61\text{A}, dI_{SD}/dt = 100\text{A}/\mu\text{s}$	-	-	45	nC

Notes:

1: Starting $T_J = 25^\circ\text{C}$, $L = 0.228\text{mH}$, $I_{AS} = 40\text{A}$.

2: Pulse Width = 100s

Typical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

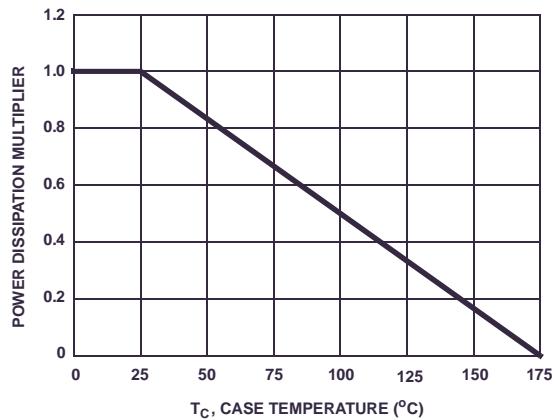


Figure 1. Normalized Power Dissipation vs Ambient Temperature

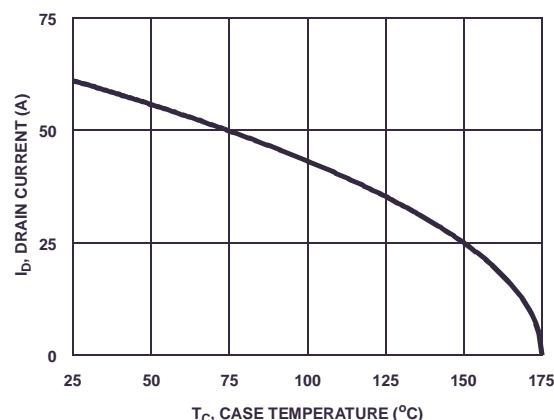


Figure 2. Maximum Continuous Drain Current vs Case Temperature

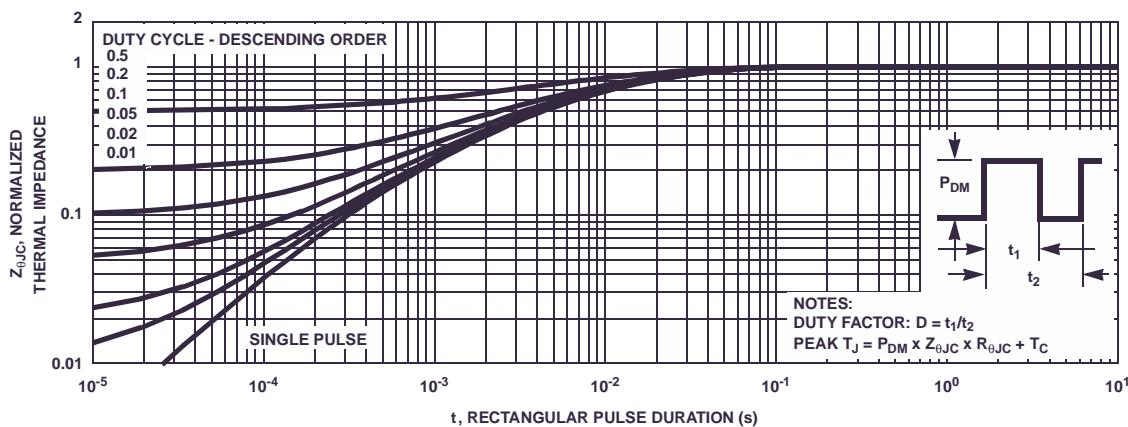


Figure 3. Normalized Maximum Transient Thermal Impedance

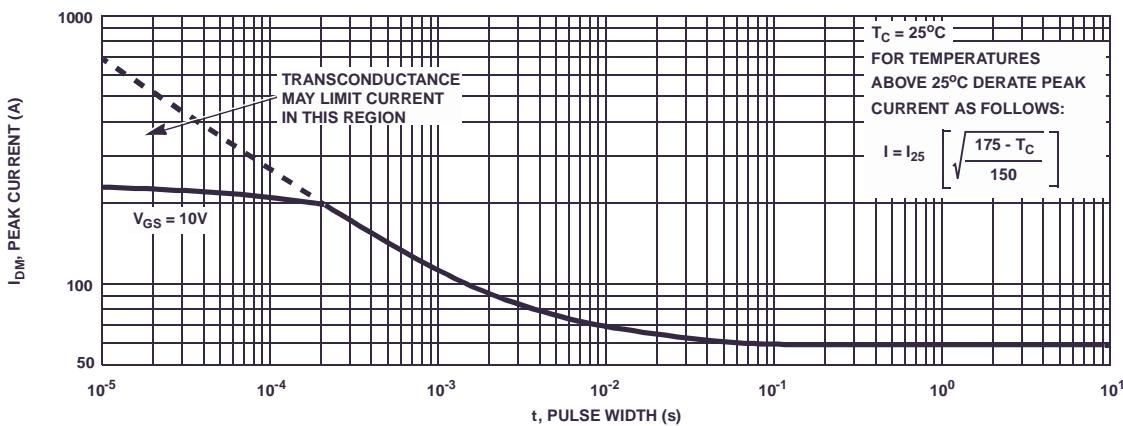


Figure 4. Peak Current Capability