



ISL9V2040D3S / ISL9V2040S3S / ISL9V2040P3

EcoSPARK™ 200mJ, 400V, N-Channel Ignition IGBT

General Description

The ISL9V2040D3S, ISL9V2040S3S, and ISL9V2040P3 are the next generation ignition IGBTs that offer outstanding SCIS capability in the space saving D-Pak (TO-252), as well as the industry standard D²-Pak (TO-263) and TO-220 plastic packages. This device is intended for use in automotive ignition circuits, specifically as a coil driver. Internal diodes provide voltage clamping without the need for external components.

EcoSPARK™ devices can be custom made to specific clamp voltages. Contact your nearest Fairchild sales office for more information.

Formerly Developmental Type 49444

Applications

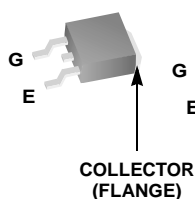
- Automotive Ignition Coil Driver Circuits
- Coil- On Plug Applications

Features

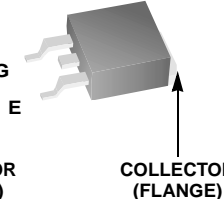
- Space saving D - Pak package available
- SCIS Energy = 200mJ at $T_J = 25^\circ\text{C}$
- Logic Level Gate Drive

Package

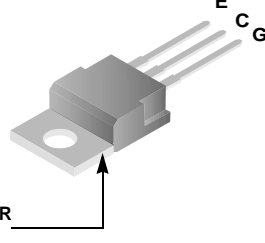
JEDEC TO-252AA
D-Pak



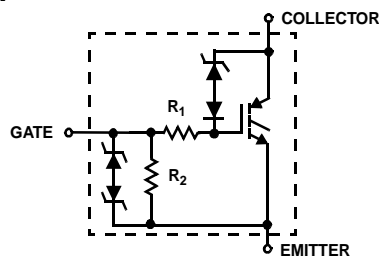
JEDEC TO-263AB
D²-Pak



JEDEC TO-220AB



Symbol



Device Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
BV_{CER}	Collector to Emitter Breakdown Voltage ($I_C = 1 \text{ mA}$)	430	V
BV_{ECS}	Emitter to Collector Voltage - Reverse Battery Condition ($I_C = 10 \text{ mA}$)	24	V
E_{SCIS25}	At Starting $T_J = 25^\circ\text{C}$, $I_{SCIS} = 11.5\text{A}$, $L = 3.0\text{mH}$	200	mJ
$E_{SCIS150}$	At Starting $T_J = 150^\circ\text{C}$, $I_{SCIS} = 8.9\text{A}$, $L = 3.0\text{mH}$	120	mJ
I_{C25}	Collector Current Continuous, At $T_C = 25^\circ\text{C}$, See Fig 9	10	A
I_{C110}	Collector Current Continuous, At $T_C = 110^\circ\text{C}$, See Fig 9	10	A
V_{GEM}	Gate to Emitter Voltage Continuous	± 10	V
P_D	Power Dissipation Total $T_C = 25^\circ\text{C}$	130	W
	Power Dissipation Derating $T_C > 25^\circ\text{C}$	0.87	W/ $^\circ\text{C}$
T_J	Operating Junction Temperature Range	-40 to 175	$^\circ\text{C}$
T_{STG}	Storage Junction Temperature Range	-40 to 175	$^\circ\text{C}$
T_L	Max Lead Temp for Soldering (Leads at 1.6mm from Case for 10s)	300	$^\circ\text{C}$
T_{pkg}	Max Lead Temp for Soldering (Package Body for 10s)	260	$^\circ\text{C}$
ESD	Electrostatic Discharge Voltage at 100pF, 1500 Ω	4	kV

Package Marking and Ordering Information

Device Marking	Device	Package	Tape Width	Quantity
V2040D	ISL9V2040D3S	TO-252AA	16mm	2500
V2040S	ISL9V2040S3S	TO-263AB	24mm	800
V2040P	ISL9V2040P3	TO-220AB	-	-

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off State Characteristics						
BV_{CER}	Collector to Emitter Breakdown Voltage	$I_C = 2\text{mA}$, $V_{\text{GE}} = 0$, $R_G = 1\text{K}\Omega$, See Fig. 15 $T_J = -40$ to 150°C	370	400	430	V
BV_{CES}	Collector to Emitter Breakdown Voltage	$I_C = 10\text{mA}$, $V_{\text{GE}} = 0$, $R_G = 0$, See Fig. 15 $T_J = -40$ to 150°C	390	420	450	V
BV_{ECS}	Emitter to Collector Breakdown Voltage	$I_C = -75\text{mA}$, $V_{\text{GE}} = 0\text{V}$, $T_C = 25^\circ\text{C}$	30	-	-	V
BV_{GES}	Gate to Emitter Breakdown Voltage	$I_{\text{GES}} = \pm 2\text{mA}$	± 12	± 14	-	V
I_{CER}	Collector to Emitter Leakage Current	$V_{\text{CER}} = 250\text{V}$, $R_G = 1\text{K}\Omega$, See Fig. 11	$T_C = 25^\circ\text{C}$	-	-	25 μA
			$T_C = 150^\circ\text{C}$	-	-	1 mA
I_{ECS}	Emitter to Collector Leakage Current	$V_{\text{EC}} = 24\text{V}$, See Fig. 11	$T_C = 25^\circ\text{C}$	-	-	1 mA
			$T_C = 150^\circ\text{C}$	-	-	40 mA
R_1	Series Gate Resistance		-	70	-	Ω
R_2	Gate to Emitter Resistance		10K	-	26K	Ω

On State Characteristics

$V_{\text{CE(SAT)}}$	Collector to Emitter Saturation Voltage	$I_C = 6\text{A}$, $V_{\text{GE}} = 4\text{V}$	$T_C = 25^\circ\text{C}$, See Fig. 3	-	1.45	1.9	V
$V_{\text{CE(SAT)}}$	Collector to Emitter Saturation Voltage	$I_C = 10\text{A}$, $V_{\text{GE}} = 4.5\text{V}$	$T_C = 150^\circ\text{C}$ See Fig. 4	-	1.95	2.3	V

Dynamic Characteristics

Q _{G(ON)}	Gate Charge	I _C = 10A, V _{CE} = 12V, V _{GE} = 5V, See Fig. 14		-	12	-	nC
V _{GE(TH)}	Gate to Emitter Threshold Voltage	I _C = 1.0mA, V _{CE} = V _{GE} , See Fig. 10	T _C = 25°C	1.3	-	2.3	V
			T _C = 150°C	0.75	-	1.8	V
V _{GEP}	Gate to Emitter Plateau Voltage	I _C = 10A, V _{CE} = 12V		-	3.4	-	V

Switching Characteristics

$t_{\text{d(ON)R}}$	Current Turn-On Delay Time-Resistive	$V_{\text{CE}} = 14\text{V}$, $R_L = 1\Omega$ $V_{\text{GE}} = 5\text{V}$, $R_G = 1\text{K}\Omega$ $T_J = 25^\circ\text{C}$	-	0.61	-	μs
t_{riseR}	Current Rise Time-Resistive		-	2.17	-	μs
$t_{\text{d(OFF)L}}$	Current Turn-Off Delay Time-Inductive	$V_{\text{CE}} = 300\text{V}$, $L = 500\mu\text{H}$, $V_{\text{GE}} = 5\text{V}$, $R_G = 1\text{K}\Omega$ $T_J = 25^\circ\text{C}$, See Fig. 12	-	3.64	-	μs
t_{fL}	Current Fall Time-Inductive		-	2.36	-	μs
SCIS	Self Clamped Inductive Switching	$T_J = 25^\circ\text{C}$, $L = 3.0\text{mH}$, $R_G = 1\text{K}\Omega$, $V_{\text{GE}} = 5\text{V}$, See Fig. 1 & 2	-	-	200	mJ

Thermal Characteristics

$R_{\theta\text{JC}}$	Thermal Resistance Junction-Case	TO-252, TO-263, TO-220	-	-	1.15	$^\circ\text{C/W}$
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Typical Performance Curves (Continued)

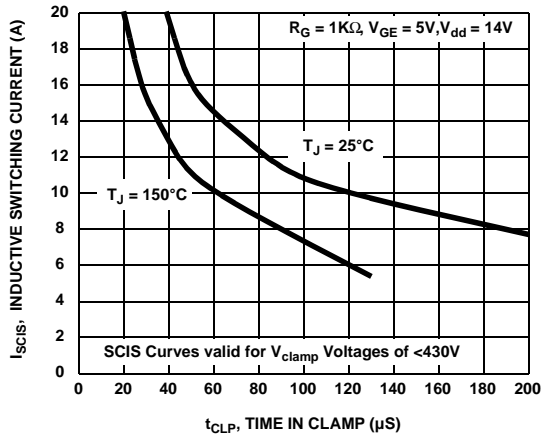


Figure 1. Self Clamped Inductive Switching Current vs Time in Clamp

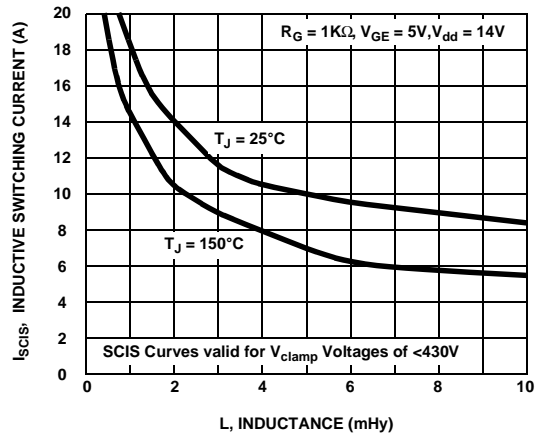


Figure 2. Self Clamped Inductive Switching Current vs Inductance

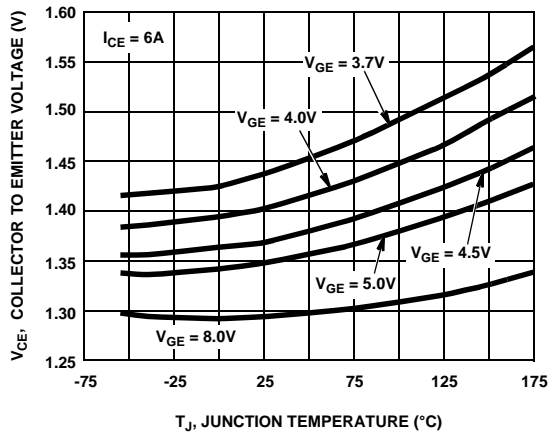


Figure 3. Collector to Emitter On-State Voltage vs Junction Temperature

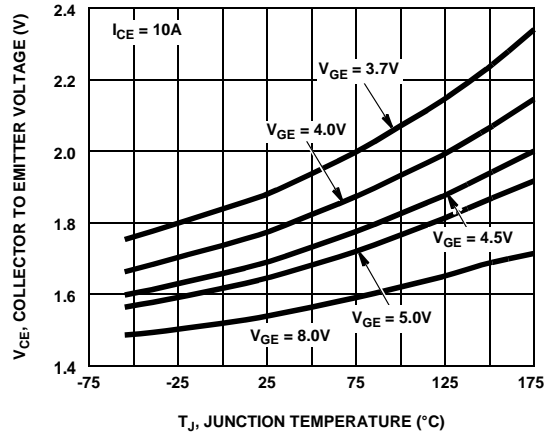


Figure 4. Collector to Emitter On-State Voltage vs Junction Temperature

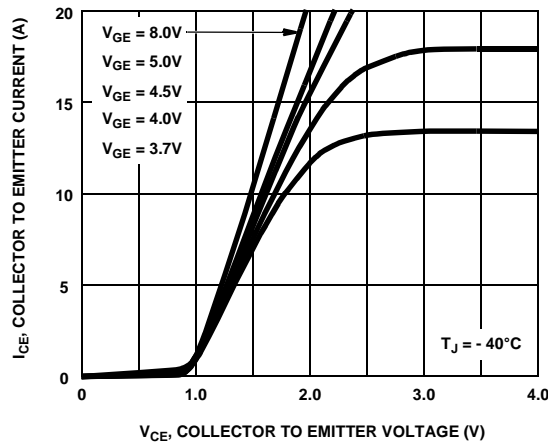


Figure 5. Collector to Emitter On-State Voltage vs Collector Current

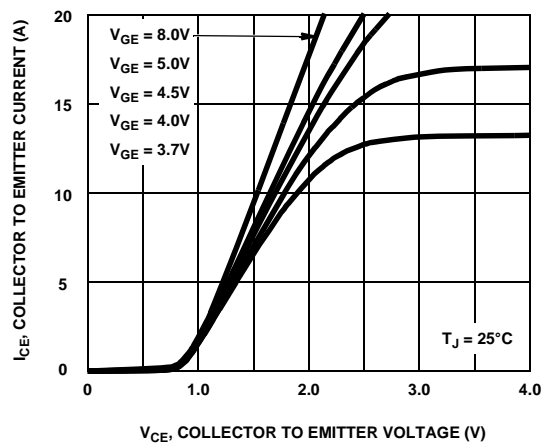


Figure 6. Collector to Emitter On-State Voltage vs Collector Current