

N-Channel MOSFET Transistor

FDD9407

• FEATURES

- With TO-252(DPAK) packaging
- UIS capability
- High speed switching
- Easy to use
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operationz

• APPLICATIONS

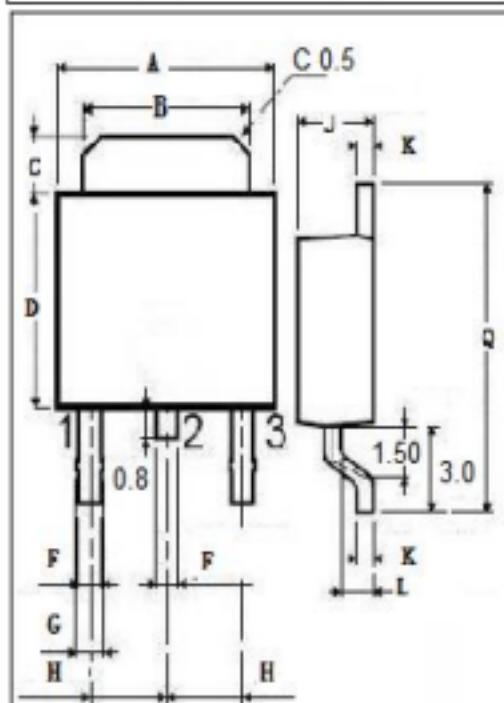
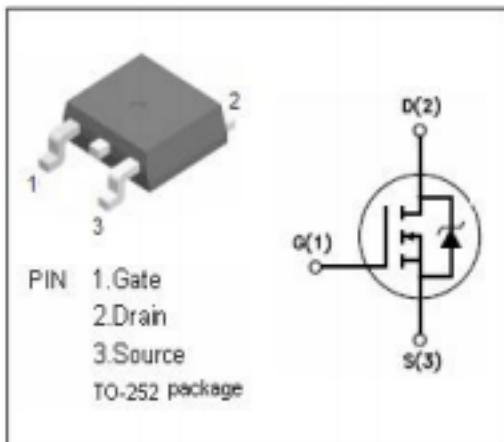
- Switching applications

• ABSOLUTE MAXIMUM RATINGS($T_J=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	100	A
$I_{DS(on)}$	Drain Current-Single Pulsed	500	A
P_D	Total Dissipation	227	W
T_J	Operating Junction Temperature	-55~175	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~175	$^\circ\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	0.66	$^\circ\text{C/W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	52	$^\circ\text{C/W}$



DIM	mm	
	MIN	MAX
A	6.40	6.60
B	5.20	5.40
C	1.15	1.35
D	5.70	6.10
F	0.65	
G	0.75	
H	2.10	2.50
J	2.10	2.40
K	0.40	0.60
L	0.90	1.10
Q	9.90	10.1

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ELECTRICAL CHARACTERISTICS

 $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$\text{BV}_{\text{DS}(\text{SS})}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D=0.25\text{mA}$	40			V
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\pm 20\text{V}; \text{I}_D=0.25\text{mA}$	2.0		4.0	V
$\text{R}_{\text{DS(on)}}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}=10\text{V}; \text{I}_D=80\text{A}$		1.6	2	$\text{m}\Omega$
I_{SS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}= \pm 20\text{V}; \text{V}_{\text{DS}}= 0\text{V}$			± 1	μA
$\text{I}_{\text{DS}}^{\text{off}}$	Drain-Source Leakage Current	$\text{V}_{\text{GS}}= 40\text{V}; \text{V}_{\text{DS}}= 0\text{V}; \text{T}_j=25^\circ\text{C}$ $\text{T}_j=175^\circ\text{C}$			1 1000	μA
$\text{V}_{\text{SD}}^{\text{on}}$	Diode forward voltage	$\text{I}_{\text{SD}}=80\text{A}, \text{V}_{\text{GS}} = 0\text{ V}$			1.25	V