

Low drop power Schottky rectifier

Features

- Very small conduction losses
- Negligible switching losses
- Extremely fast switching
- Low forward voltage drop
- Low thermal resistance
- Avalanche capability specified

Description

This dual center tap Schottky rectifier is suited for switch mode power supplies and high frequency DC to DC converters.

Packaged in TO-220AB, D²PAK and I²PAK, this device is intended for use in low voltage, high frequency inverters, free-wheeling and polarity protection applications.

Table 1. Device summary

I _{F(AV)}	2 x 15 A
V _{RRM}	30 V
T _j (max)	150 °C
V _{F(typ)}	0.37 V

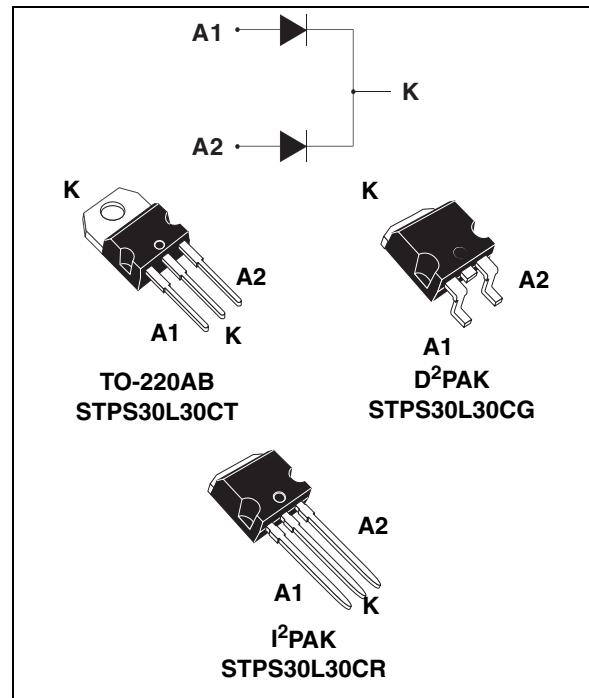
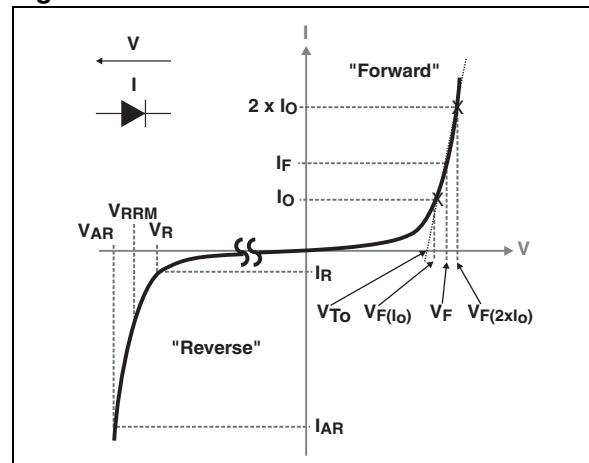


Figure 1. Electrical characteristics (a)



- a. V_{ARM} and I_{ARM} must respect the reverse safe operating area defined in [Figure 12](#). V_{AR} and I_{AR} are pulse measurements ($t_p < 1 \mu\text{s}$). V_R, I_R, V_{RRM} and V_F, are static characteristics

1 Characteristics

Table 2. Absolute ratings (limiting values per diode)

Symbol	Parameter			Value	Unit
V _{RRM}	Repetitive peak reverse voltage			30	V
I _{F(RMS)}	Forward rms current			30	A
I _{F(AV)}	Average forward current $\delta = 0.5$	T _c = 140 °C, Per diode Per device		15 30	A
I _{FSM}	Surge non repetitive forward current			t _p = 10 ms sinusoidal,	220 A
I _{RRM}	Peak repetitive reverse current			t _p = 2 μs square, F= 1 kHz square	1 A
I _{RSM}	Non repetitive peak reverse current			t _p = 100 μs square	3 A
P _{ARM} ⁽¹⁾	Repetitive peak avalanche power			t _p = 1 μs T _j = 25 °C	5300 W
V _{ARM} ⁽²⁾	Maximum repetitive peak avalanche voltage			t _p < 1 μs T _j < 150 °C I _{AR} < 35 A	45 V
V _{ASM} ⁽²⁾	Maximum single pulse peak avalanche voltage			t _p < 1 μs T _j < 150 °C I _{AR} < 35 A	45 V
T _{stg}	Storage temperature range			-65 to + 175	°C
T _j	Maximum operating junction temperature ⁽³⁾			150	°C
dV/dt	Critical rate of rise of reverse voltage			10000	V/μs

1. For temperature or pulse time duration deratings, refer to [Figure 4.](#) and [Figure 5.](#). More details regarding the avalanche energy measurements and diode validation in the avalanche are provided in the application notes AN1768 and AN2025.

2. Refer to [Figure 12](#)

3. $\frac{dP_{tot}}{dT_j} < \frac{1}{R_{th(j-a)}}$ condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistance⁽¹⁾

Symbol	Parameter		Value	Unit
R _{th(j-c)}	Junction to case	Per diode	1.5	°C/W
R _{th(c)}		Total	0.8	
Coupling		0.1		

1. When the diodes 1 and 2 are used simultaneously: $\Delta T_j(\text{diode 1}) = P(\text{diode1}) \times R_{th(j-c)}(\text{Per diode}) + P(\text{diode 2}) \times R_{th(c)}$

Table 4. Static electrical characteristics (per diode)

Symbol	Parameter	Test conditions		Min.	Typ.	Max.	Unit
I _R ⁽¹⁾	Reverse leakage current	T _j = 25 °C	V _R = V _{RRM}			1.5	mA
		T _j = 125 °C			170	350	mA
V _F ⁽¹⁾	Forward voltage drop	T _j = 25 °C	I _F = 15 A			0.46	V
		T _j = 125 °C				0.33	
		T _j = 25 °C	I _F = 30A			0.57	
		T _j = 125 °C				0.43	

1. Pulse test: t_p = 380 μs, δ < 2%

To evaluate the conduction losses use the following equation:

$$P = 0.24 \times I_{F(AV)} + 0.009 \times I_{F(RMS)}^2$$

3 Ordering information

Table 8. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS30L30CT	STPS30L30CT	TO-220AB	2.0 g	50	Tube
STPS30L30CG	STPS30L30CR	D ² PAK	1.8 g	50	Tube
STPS30L30CG-TR	STPS30L30CG	D ² PAK	1.8 g	1000	Tape and reel
STPS30L30CG-TR	STPS30L30CG	I ² PAK	1.49 g	50	Tube