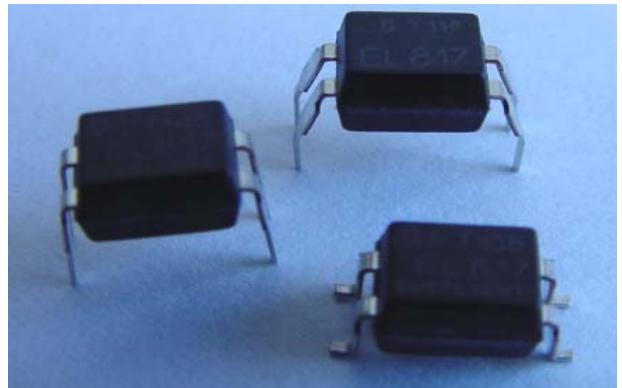


Features :

- Current transfer ratio
(CTR:MIN.50% at IF =5mA ,VCE =5V)
- High isolation voltage between input and output (Viso=5000 V rms)
- Compact dual-in-line package
EL817L*:1-channel type
- Pb free
- UL approved (No. E214129)
- VDE approved (No. 132249)
- SEMKO approved (No. 0143133/01-03)
- NEMKO approved (No. P00102385)
- DEMKO approved (No. 310352-04)
- FIMKO approved (No. FI 16763A2)
- CSA approved (No. 1143601)
- BSI approved (No. 8592 / 8593)
- Options available:
 - Leads with 0.4"(10.16mm) spacing (M Type)
 - Leads bends for surface mounting (S Type)
 - Tape and Reel of Type I for SMD(Add"-TA" Suffix)
 - Tape and Reel of Type II for SMD(Add"-TB" Suffix)
 - The tape is 16mm and is wound on a 33cm reel
- The product itself will remain within RoHS compliant version.

EL817L Series



Description

The EL817 series contains a infrared emitting diode optically coupled to a phototransistor. It is packaged in a 4-pin DIP package and available in wide-lead spacing and SMD option.

Applications

- Computer terminals
 - System appliances, measuring instruments
 - Registers, copiers, automatic vending machines
 - Cassette type recorder
 - Electric home appliances, such as fan heaters, etc.
 - Signal transmission between circuits of different potentials
-

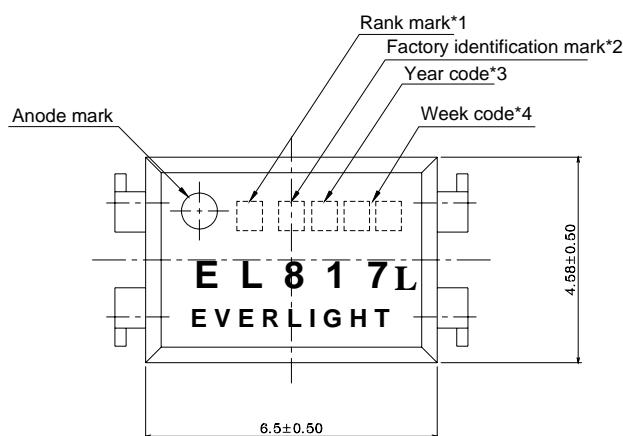
and impedances

EL817L Series

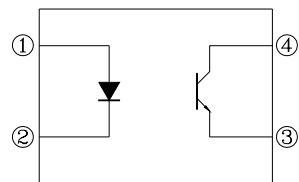
Device Selection Guide

Part. No.	Chip Material	
	IR	PT
EL817*	GaAs	Silicon

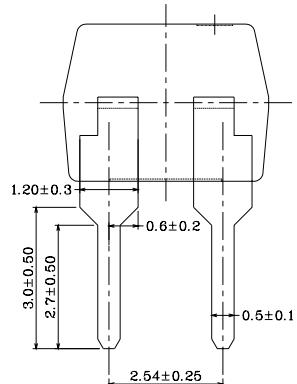
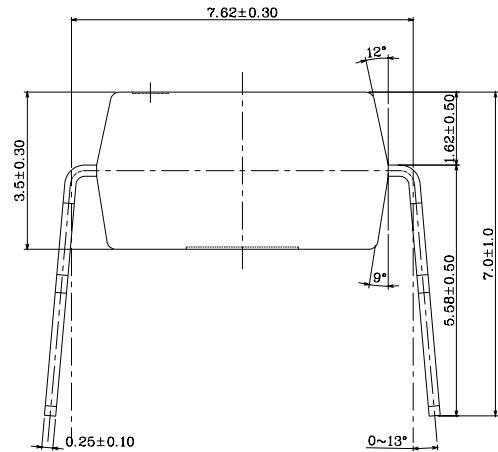
Package Dimensions



PIN NO. AND INTERNAL CONNECTION DIAGRAM



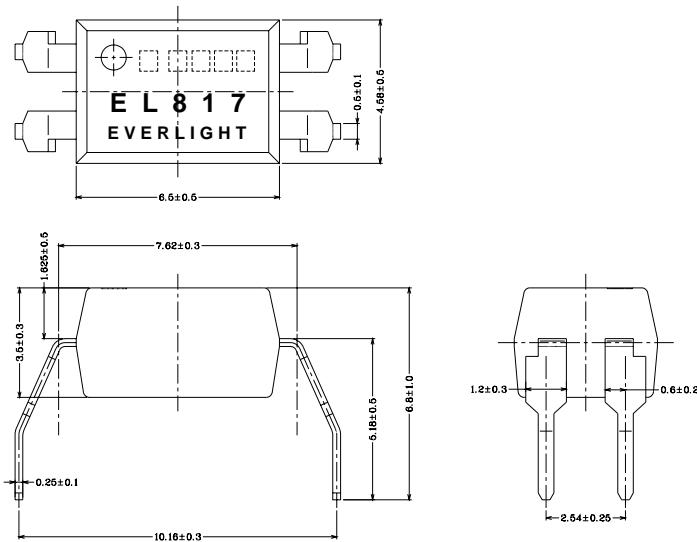
1. Anode 3. Emitter
2. Cathode 4. Collector



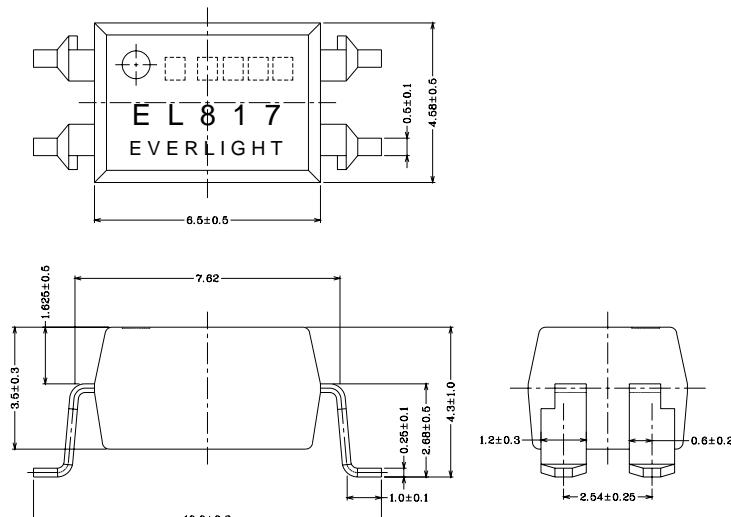
EL817L Series

Package Dimensions

M Type



S Type



Notes:

1. Rank shall be or shall not be marked
 2. Factory code shall be marked (T: Taiwan / C: China)
 3. Year date code
 4. 2-digit work week
 5. All dimensions are in millimeters
 6. Specifications are subject to change without notice
-

EL817L Series

Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Rating	Unit
Input	Forward Current	I _F	80	mA
	Reverse Voltage	V _R	6	V
	Power Dissipation	P	150	mW
Output	Collector Power Dissipation	P _C	150	mW
	Collector Current	I _C	50	mA
	Collector-Emitter Voltage	V _{CEO}	35	V
	Emitter-Collector Voltage	V _{ECO}	6	V
Total Power Dissipation		P _{tot}	200	mW
* ¹ Isolation Voltage		V _{iso}	5000	V rms
Operating Temperature		T _{opr}	-55~+110	°C
Storage Temperature		T _{stg}	-55~+125	°C
* ² Soldering Temperature		T _{sol}	260	°C

*¹ AC for 1 minute, R.H= 40~ 60%RH

-Isolation voltage shall be measured using the following method.

- (1) Short between anode and cathode on the primary side and between collector, emitter and base on the secondary side.
- (2) The isolation voltage tester with zero-cross circuit shall be used.
- (3) The waveform of applied voltage shall be a sine wave

*² For 10 seconds

EL817L Series

Electro-Optical Characteristics

(Ta=25°C)

Parameter		Symbol	Min.	Typ.	Max.	Unit	Condition
Input	Forward	V _F	-	1.2	1.4	V	I _F =20mA
	Reverse Current	I _R	-	-	10	uA	V _R =4V
	Terminal	C _t	-	30	250	pF	V=0,f=1kHz
Output	Collector Dark current	I _{CEO}	-	-	100	nA	V _{CE} =20V
	Collector-Emitter breakdown voltage	BV _{CEO}	80	-	-	V	I _c =0.1mA
Transfer Characteristics	Current Transfer ratio	CTR	50	-	600	%	I _F =5mA , V _{CE} =5V
	Collector-Emitter saturation voltage	V _{CE(sat)}	-	0.1	0.2	V	I _F =20mA , I _c =1 mA
	Isolation resistance	R _{ISO}	5x10 ¹⁰	10 ¹¹	-	Ω	DC500V,40~60%R.H
	Floating capacitance	C _f	-	0.6	1.0	pF	V=0, f=1MHz
	Cut-off frequency	f _c	-	80	-	kHz	V _{CE} =5V, I _c =2 mA R _L =100Ω, -3dB
	Rise time	t _r	-	4	18	us	V _{CE} =2V I _c =2mA,R _L =100Ω

	Fall time	t_f	-	3	18	us	EL817L Series
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Supplement

Current Transfer Ratio CTR

Sub-Model No.	Rank mark	CTR (%)	Condition
EL817* note 1		50 to 600	$I_F = 5 \text{ mA}$ $V_{CE} = 5 \text{ V}$ $T_a = 25^\circ\text{C}$
EL817* (A)	A	80 to 160	
EL817* (B)	B	130 to 260	
EL817* (C)	C	200 to 400	
EL817* (D)	D	300 to 600	

Note1. The symbol “ * ” can be none or S or M by different leads form request

Note2. The symbol “ () ” can be CTR rank

Fig. 1 Forward Current vs.
Ambient Temperature

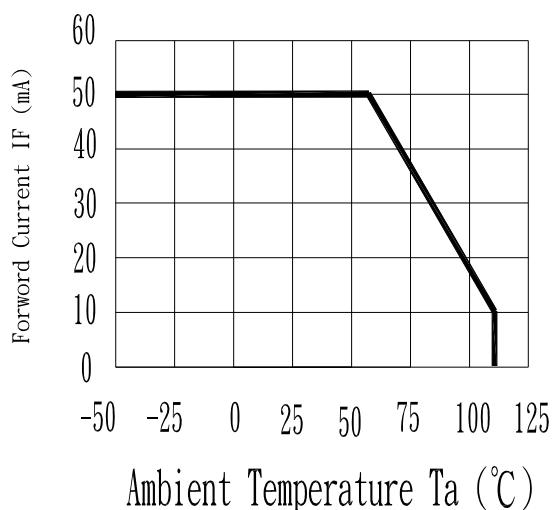


Fig. 2 Collector Power Dissipation vs.
Ambient Temperature

