



AO4407

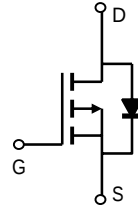
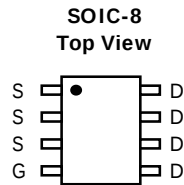
P-Channel Enhancement Mode Field Effect Transistor

General Description

The AO4407 uses advanced trench technology to provide excellent $R_{DS(ON)}$, and ultra-low low gate charge with a 25V gate rating. This device is suitable for use as a load switch or in PWM applications. Standard Product AO4407 is Pb-free (meets ROHS & Sony 259 specifications). AO4407L is a Green Product ordering option. AO4407 and AO4407L are electrically identical.

Features

V_{DS} (V) = -30V
 I_D = -12 A (V_{GS} = -20V)
 $R_{DS(ON)} < 13m\Omega$ (V_{GS} = -20V)
 $R_{DS(ON)} < 14m\Omega$ (V_{GS} = -10V)



Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 25	V
Continuous Drain Current ^A	I_D	-12	A
$T_A=25^\circ\text{C}$			
$T_A=70^\circ\text{C}$		-10	
Pulsed Drain Current ^B	I_{DM}	-60	
Power Dissipation ^A	P_D	3	W
$T_A=25^\circ\text{C}$			
$T_A=70^\circ\text{C}$		2.1	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	28	40	$^\circ\text{C/W}$
$t \leq 10\text{s}$				
Maximum Junction-to-Ambient ^A		54	75	$^\circ\text{C/W}$
Steady-State				
Maximum Junction-to-Lead ^C	$R_{\theta JL}$	21	30	$^\circ\text{C/W}$
Steady-State				

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =-250μA, V _{GS} =0V	-30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-24V, V _{GS} =0V T _J =55°C			-1 -5	μA
I _{GSS}	Gate-Body leakage current	V _{DS} =0V, V _{GS} =±25V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1.7	-2.5	-3	V
I _{D(ON)}	On state drain current	V _{GS} =-10V, V _{DS} =-5V	60			A
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-10V, I _D =-10A T _J =125°C		11 15	14 19	mΩ
		V _{GS} =-20V, I _D =-10A		10	13	mΩ
		V _{GS} =-4.5V, I _D =-10A		24		mΩ
g _{FS}	Forward Transconductance	V _{DS} =-5V, I _D =-10A		26		S
V _{SD}	Diode Forward Voltage	I _S =-1A, V _{GS} =0V		-0.72	-1	V
I _S	Maximum Body-Diode Continuous Current				-4.2	A
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-15V, f=1MHz		2076	2500	pF
C _{oss}	Output Capacitance			503		pF
C _{rss}	Reverse Transfer Capacitance			302		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		2	3	Ω
SWITCHING PARAMETERS						
Q _g	Total Gate Charge	V _{GS} =-10V, V _{DS} =-15V, I _D =-12A		37.2	45	nC
Q _{gs}	Gate Source Charge			7		nC
Q _{gd}	Gate Drain Charge			10.4		nC
t _{D(on)}	Turn-On DelayTime	V _{GS} =-10V, V _{DS} =-15V, R _L =1.25Ω, R _{GEN} =3Ω		12.4		ns
t _r	Turn-On Rise Time			8.2		ns
t _{D(off)}	Turn-Off DelayTime			25.6		ns
t _f	Turn-Off Fall Time			12		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =-12A, dI/dt=100A/μs		33	40	ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =-12A, dI/dt=100A/μs		23		nC

A: The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design. The current rating is based on the t≤ 10s thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

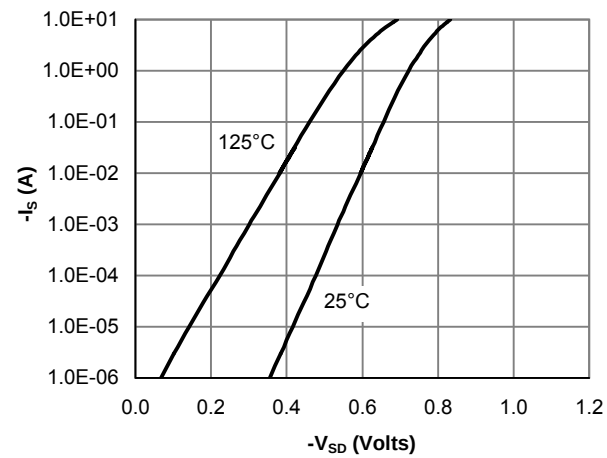
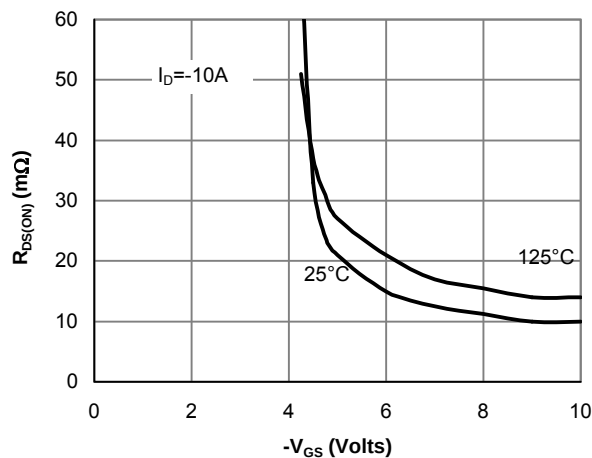
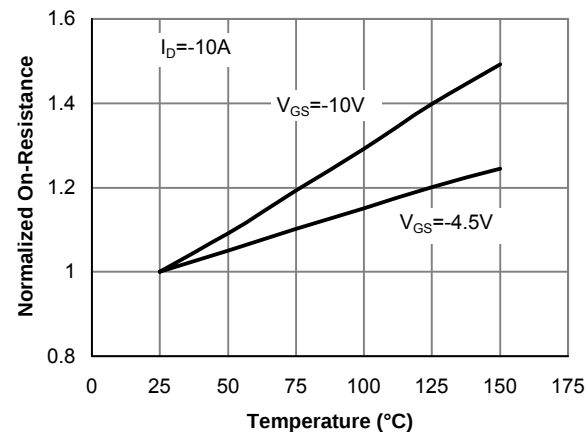
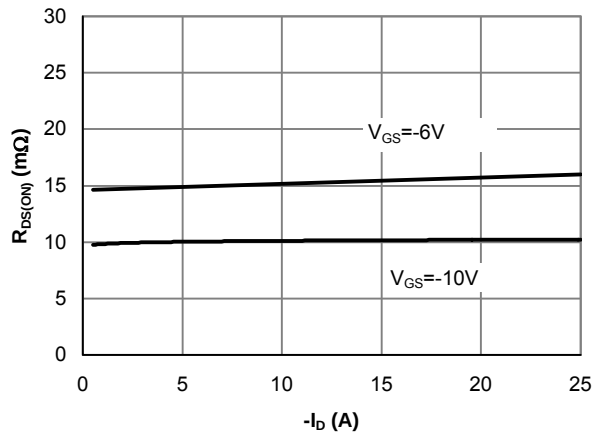
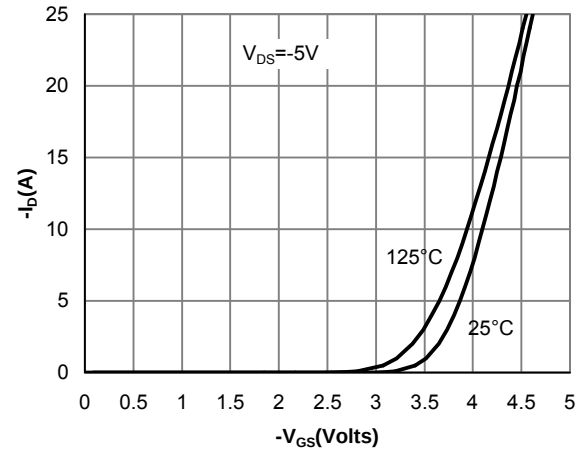
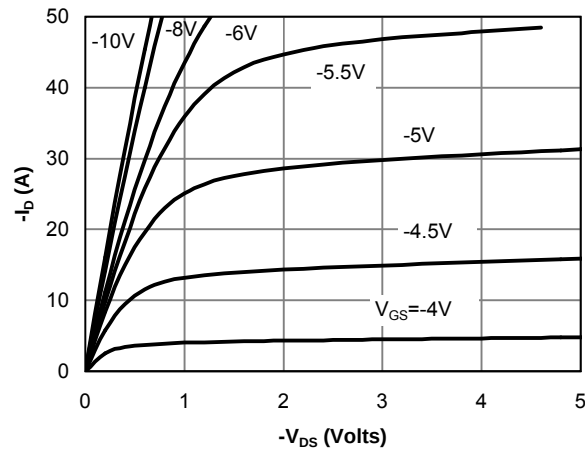
C: The R_{θJA} is the sum of the thermal impedance from junction to lead R_{θJL} and lead to ambient.

D: The static characteristics in Figures 1 to 6, 12, 14 are obtained using 80μs pulses, duty cycle 0.5% max.

E: These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The SOA curve provides a single pulse rating. Rev 1 : Sept 2005

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



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